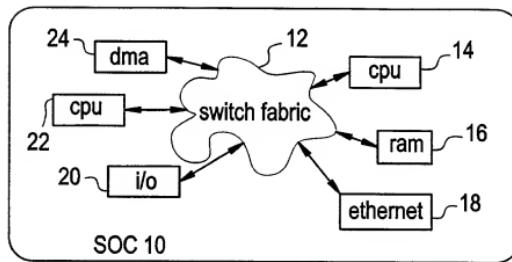


**FIG. 1**  
PRIOR ART



**FIG. 2**

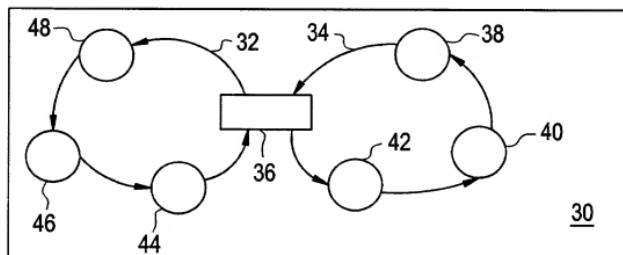


FIG. 3

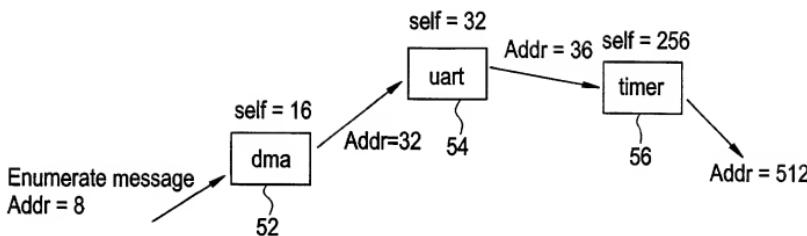


FIG. 4

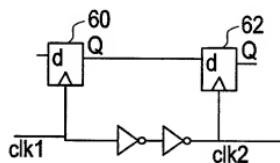


FIG. 5

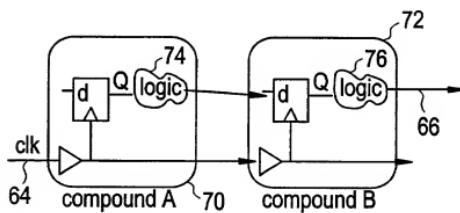


FIG. 6

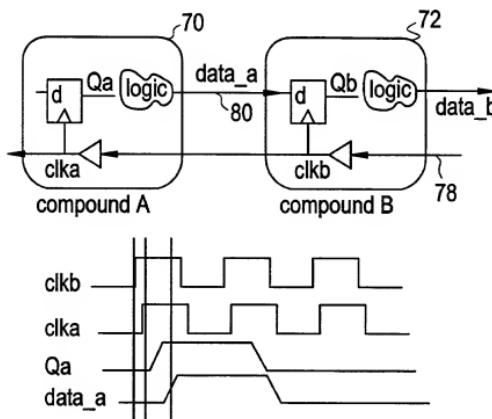
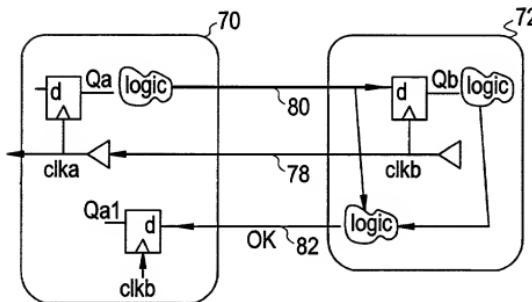


FIG. 7



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FIG. 8

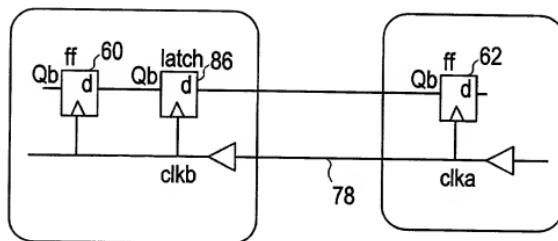
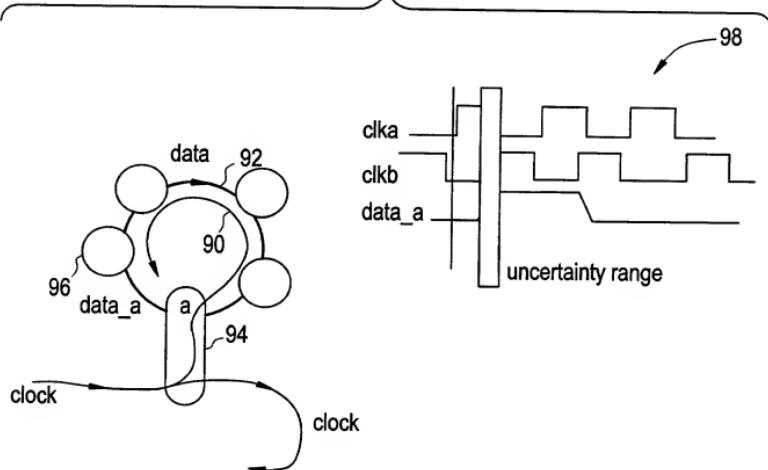


FIG. 9



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FIG. 10

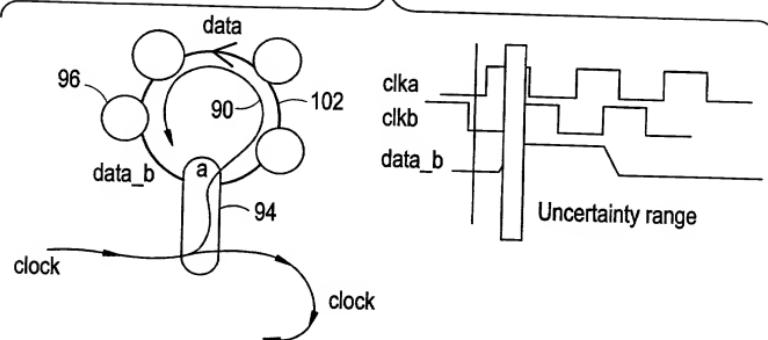


FIG. 11

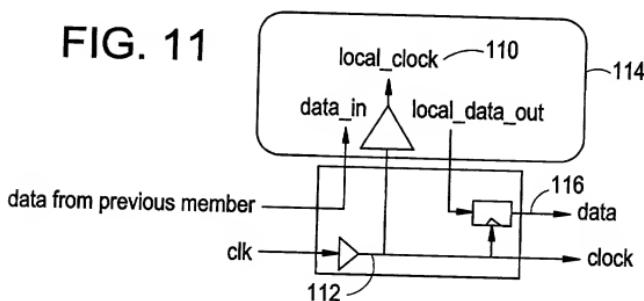


FIG. 12

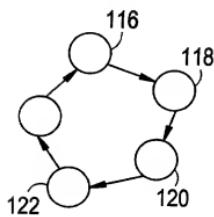
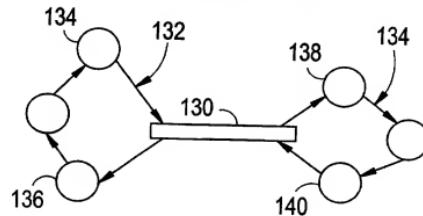


FIG. 13



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FIG. 14

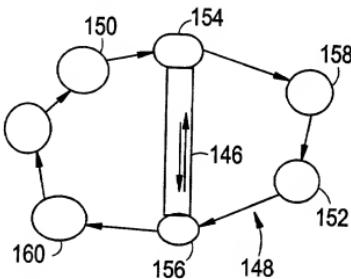


FIG. 15

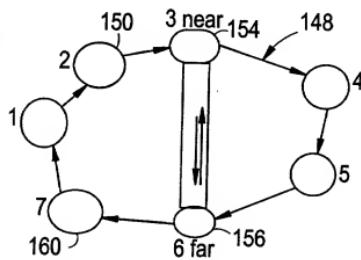


FIG. 16

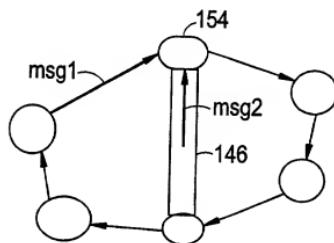


FIG. 17

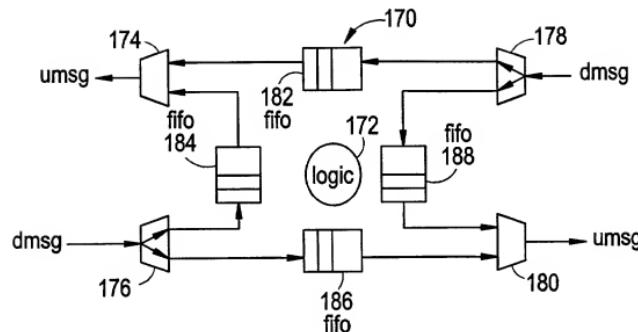


FIG. 18

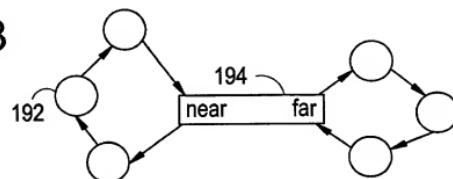
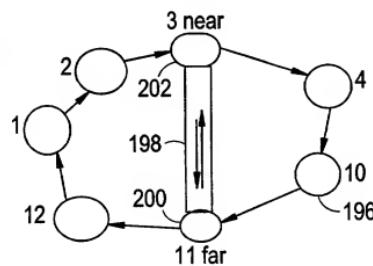
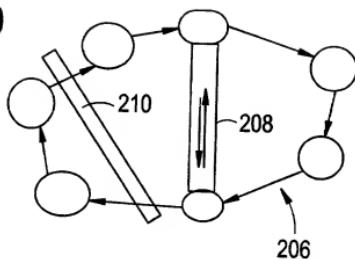


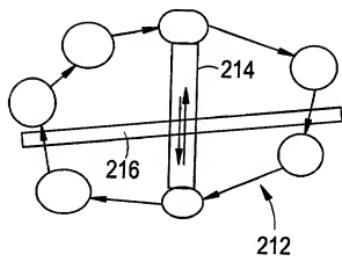
FIG. 19



**FIG. 20**



**FIG. 21**



**FIG. 22**

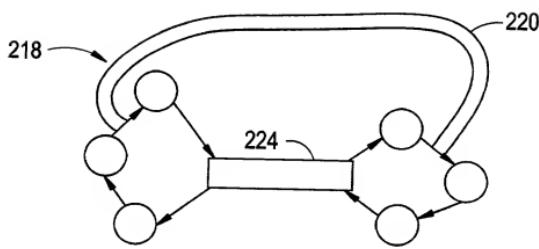




FIG. 23

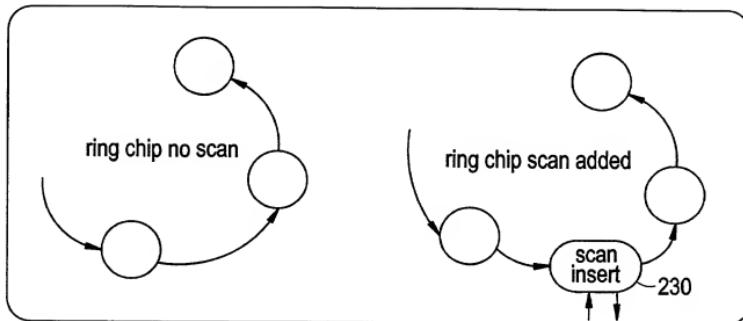


FIG. 24

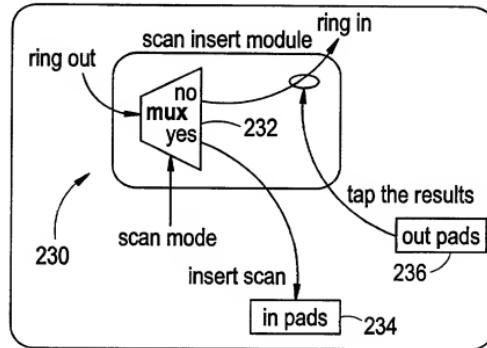


FIG. 25

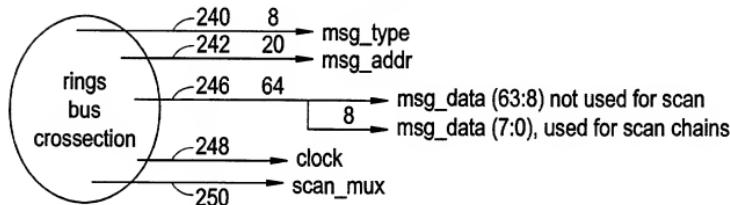
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FIG. 26

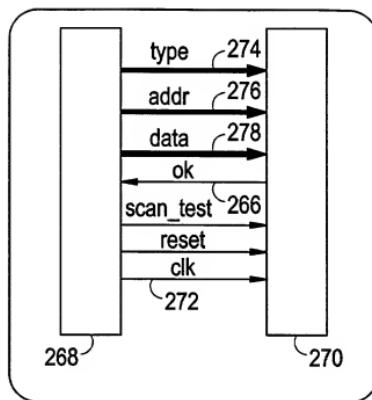
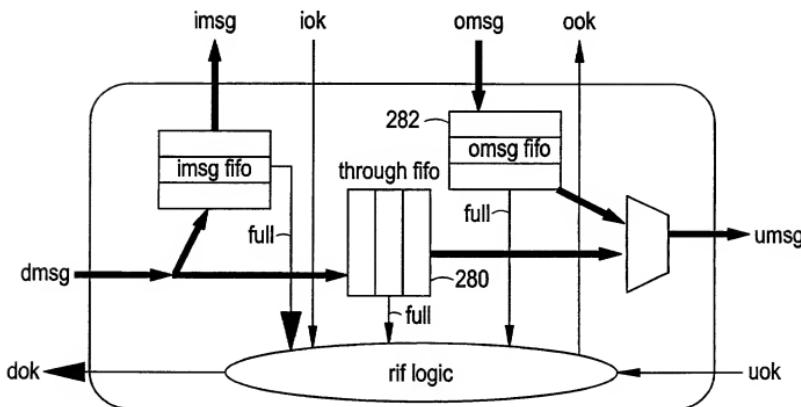


FIG. 27



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3092600

FIG. 28

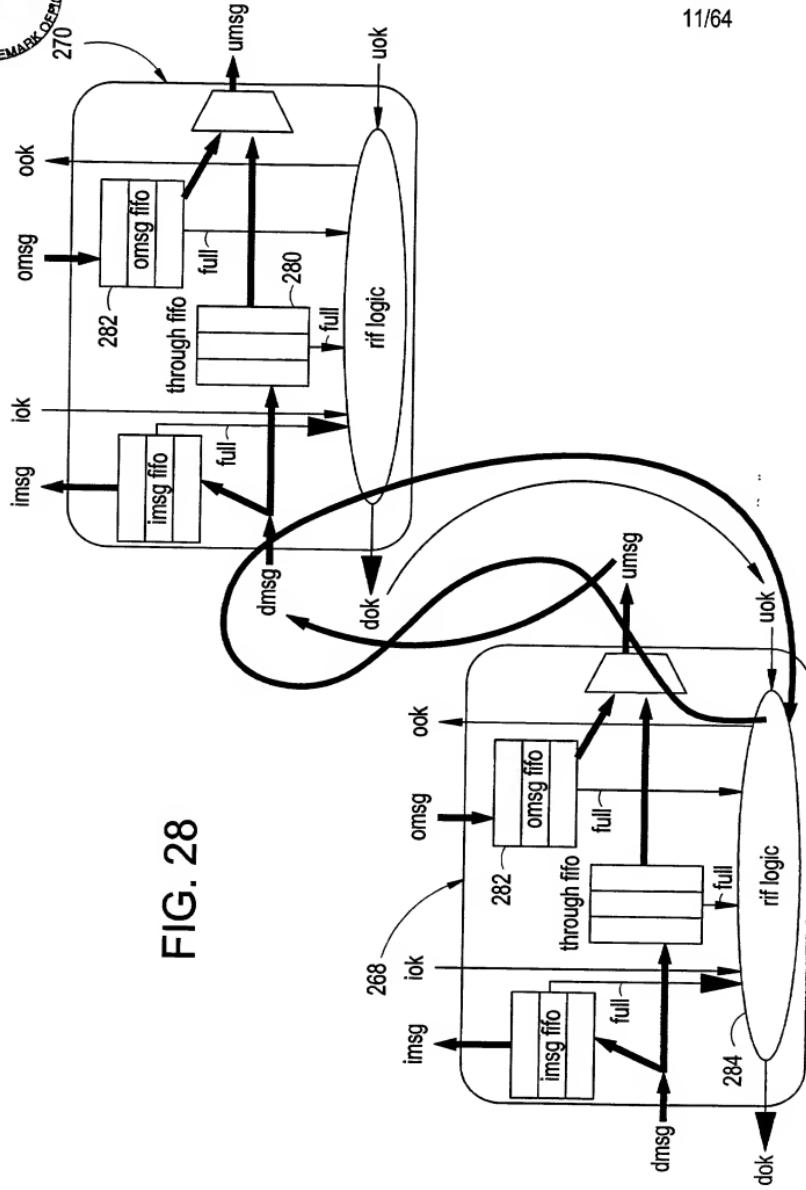
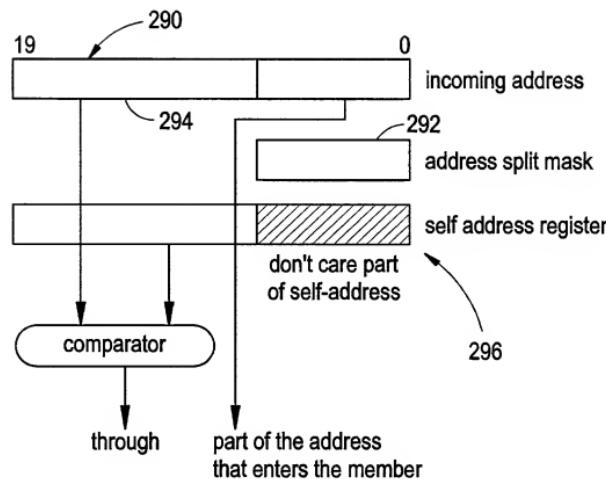


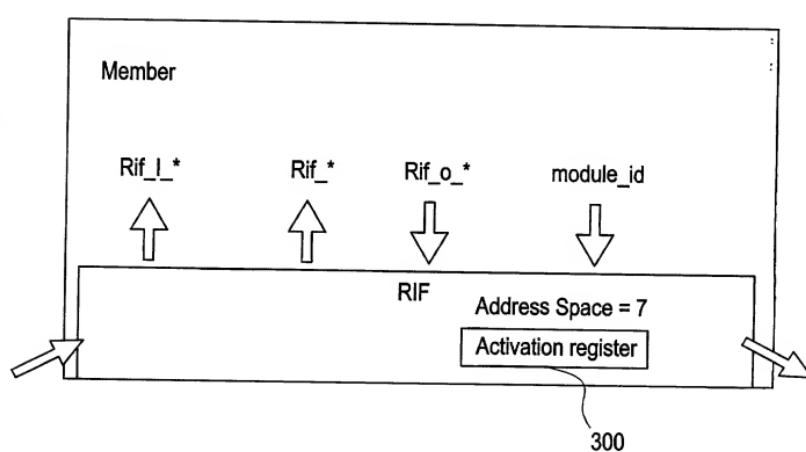
FIG. 29



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FIG. 30



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FIG. 31

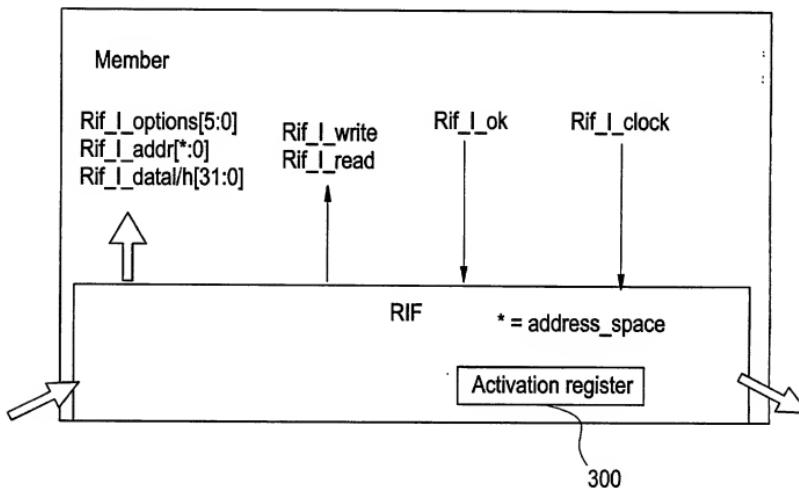
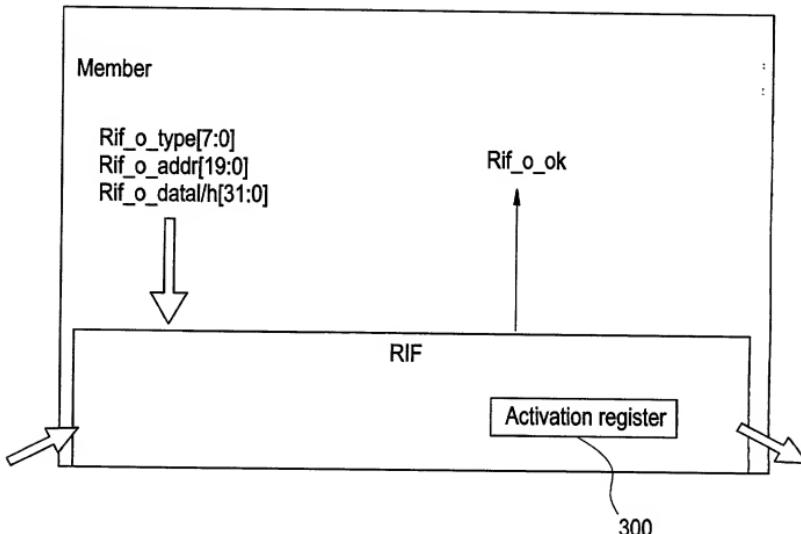


FIG. 32



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FIG. 33

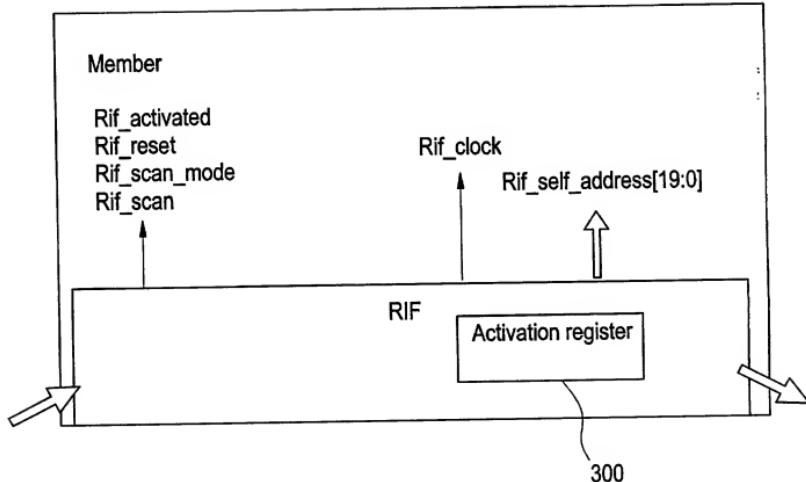


FIG. 34

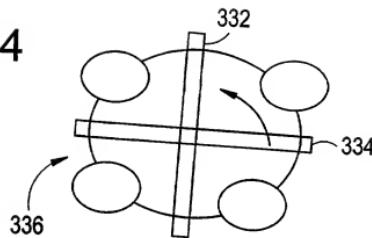


FIG. 35

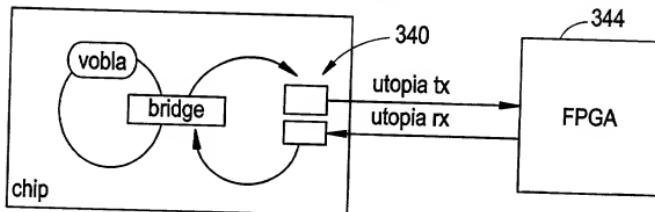
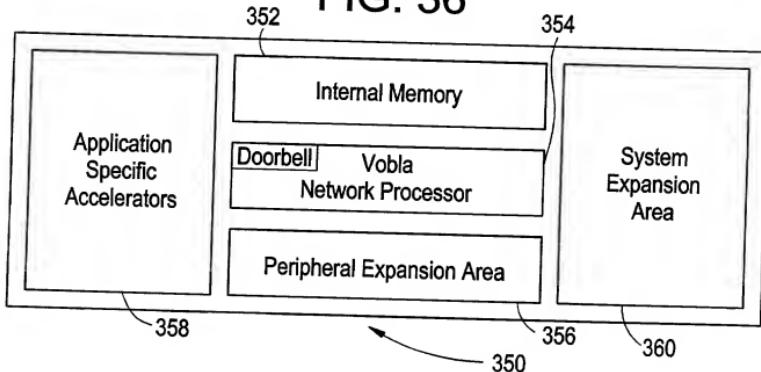


FIG. 36



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FIG. 37

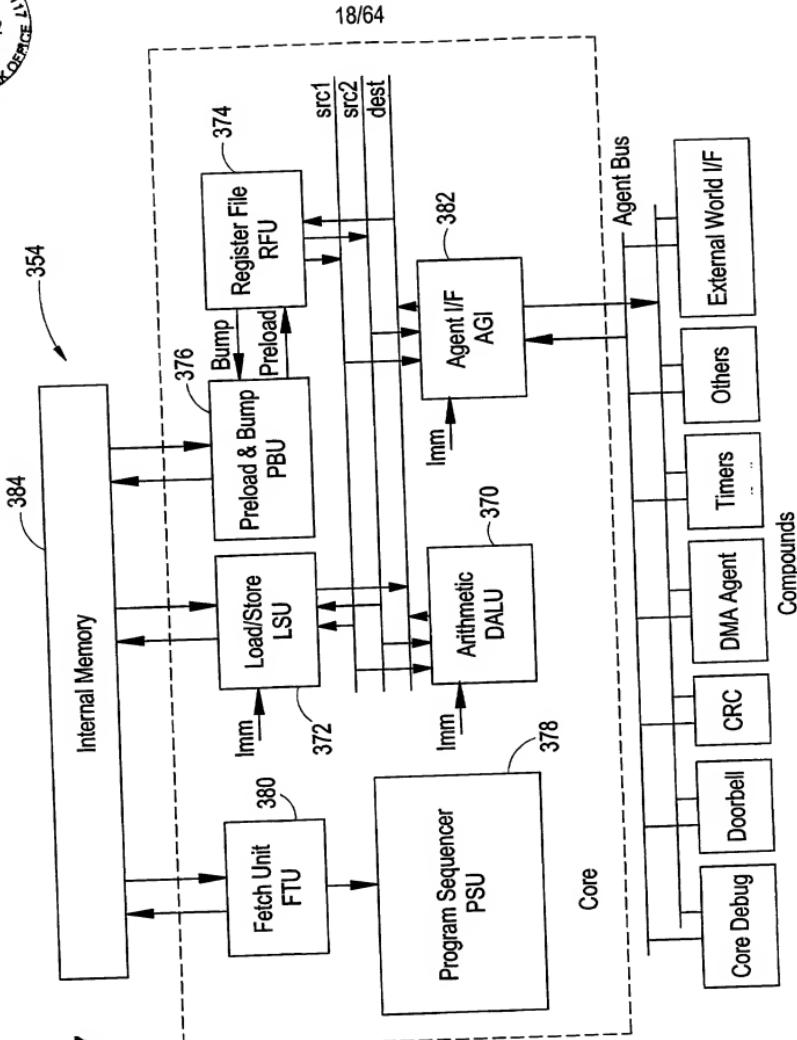


FIG. 38

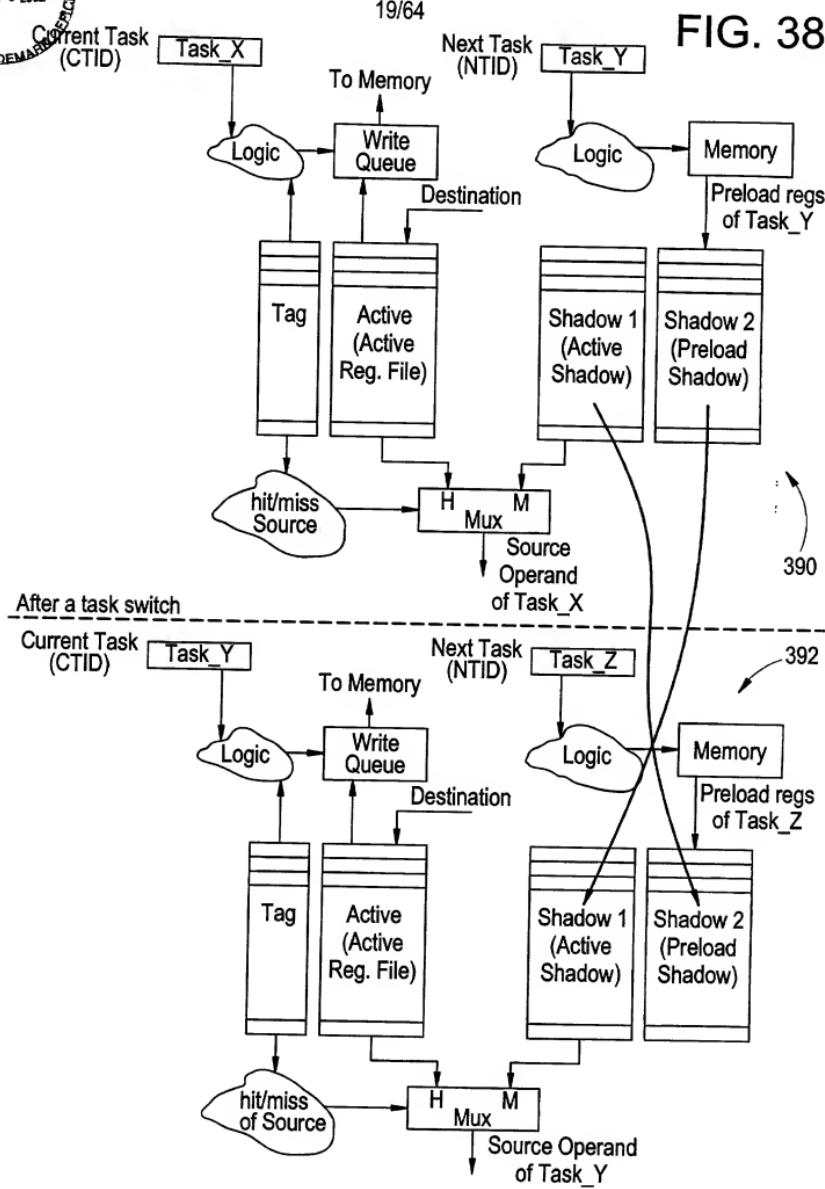
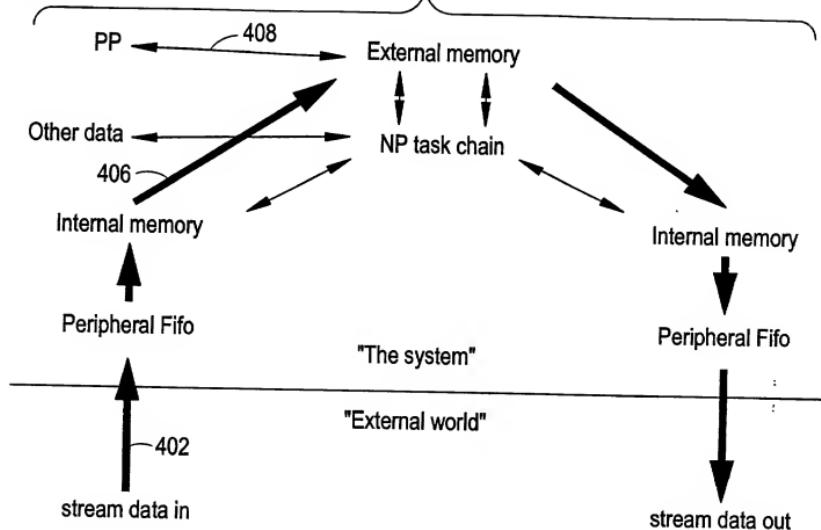


FIG. 39



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FIG. 40

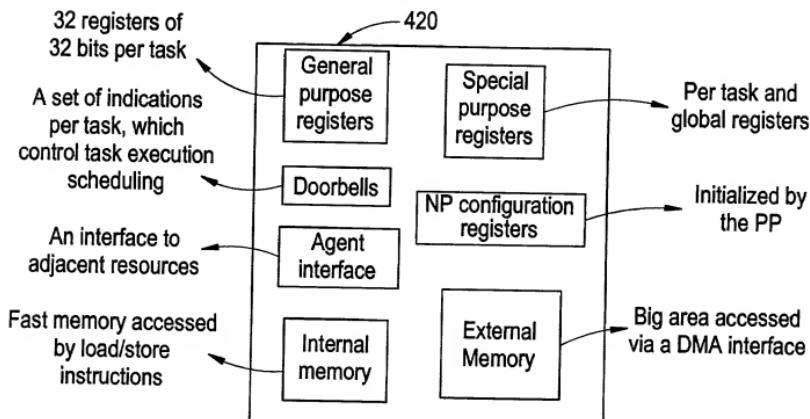
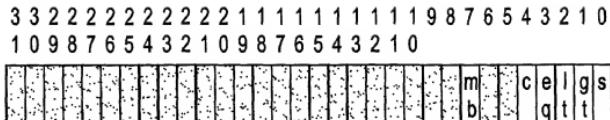




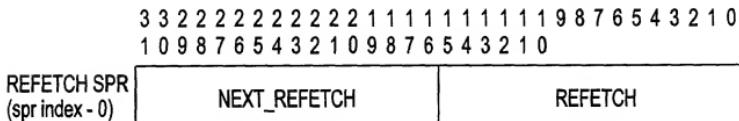
FIG. 41

R1 register



430

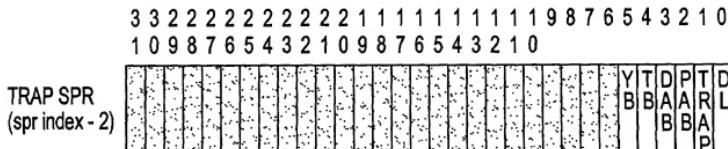
FIG. 42



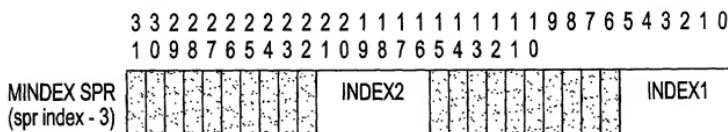
440



442



444



446

FIG. 43

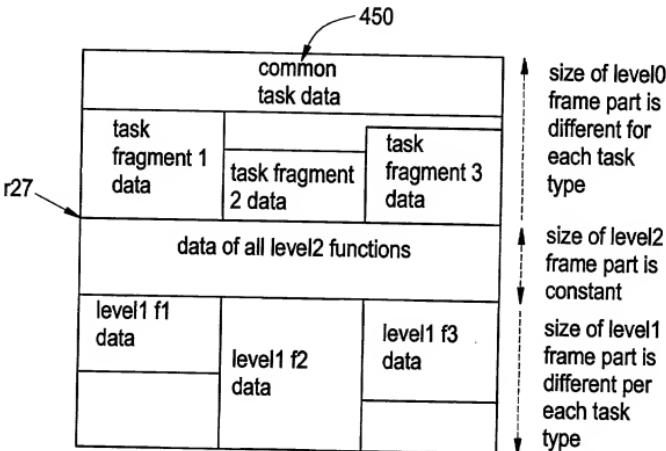


FIG. 44

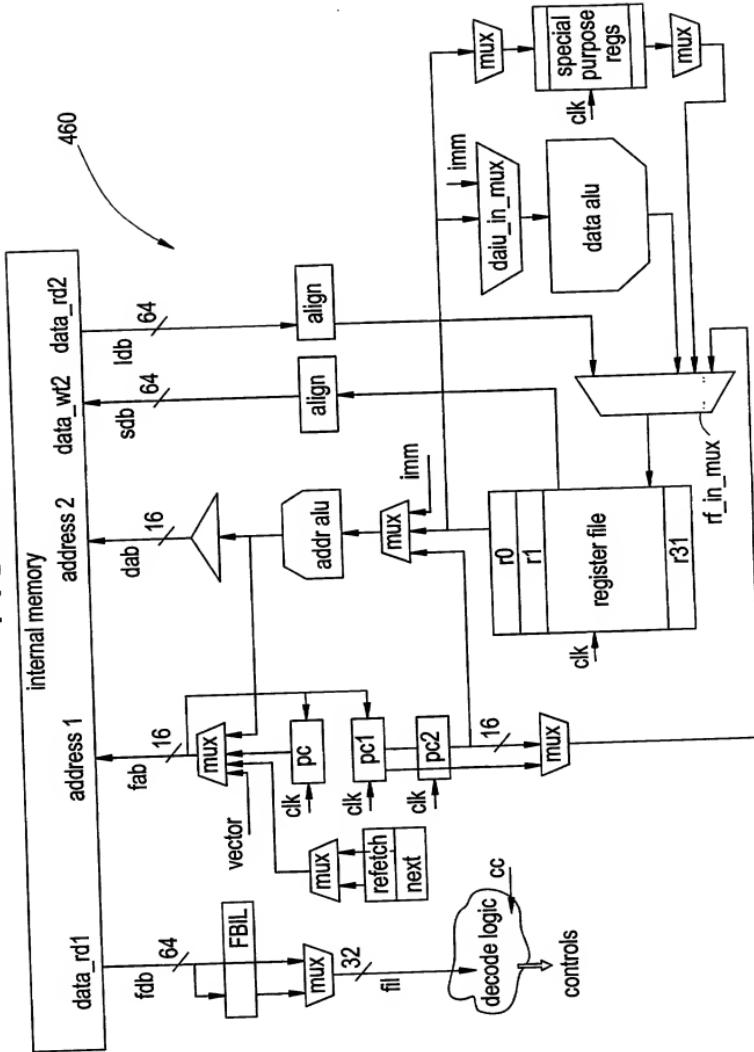


FIG. 45

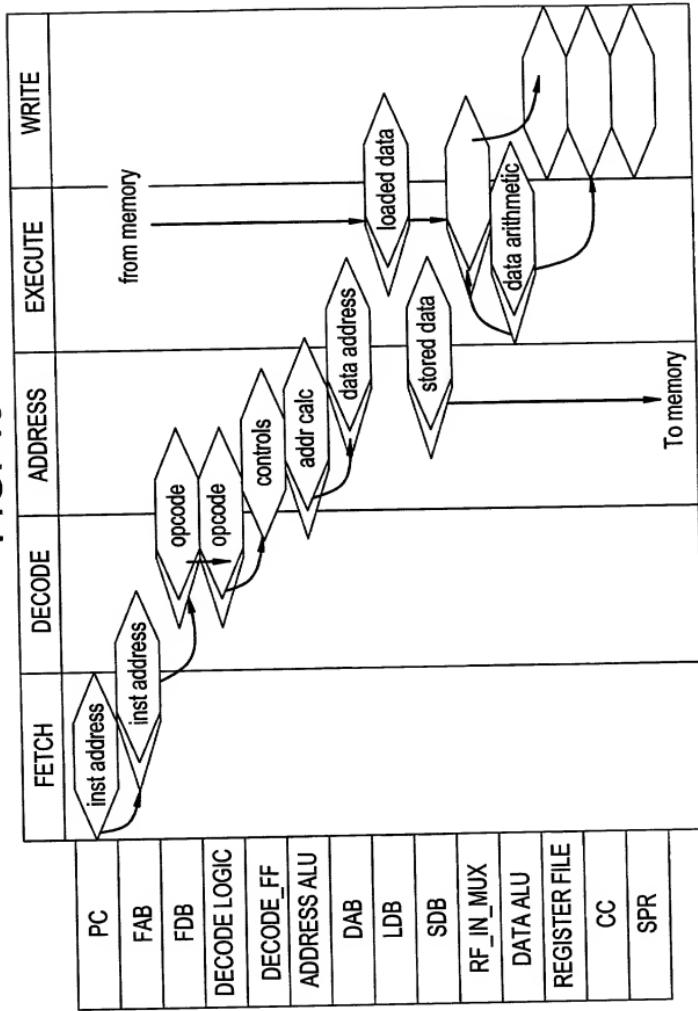




FIG. 46

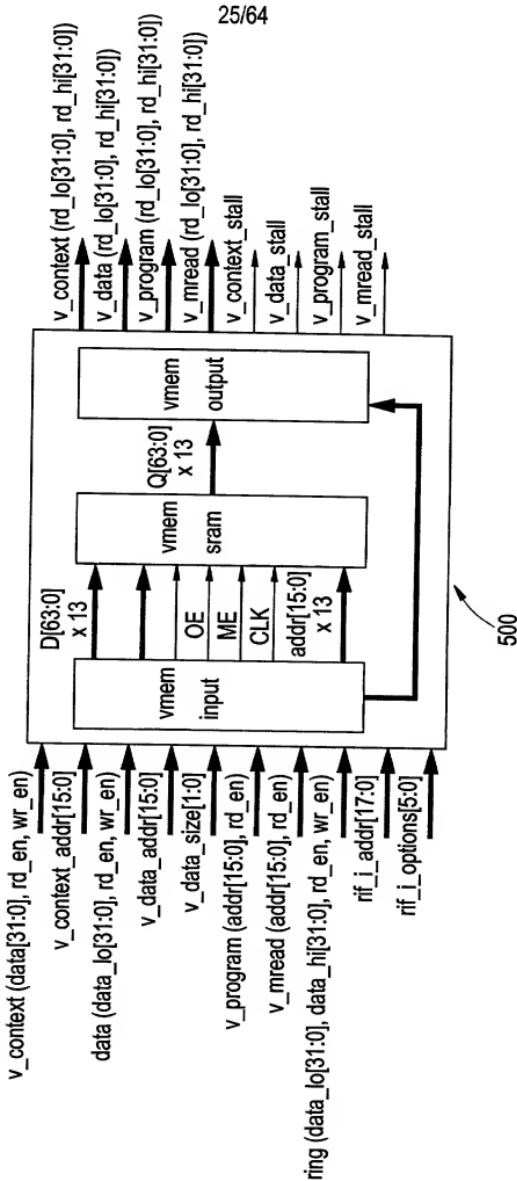
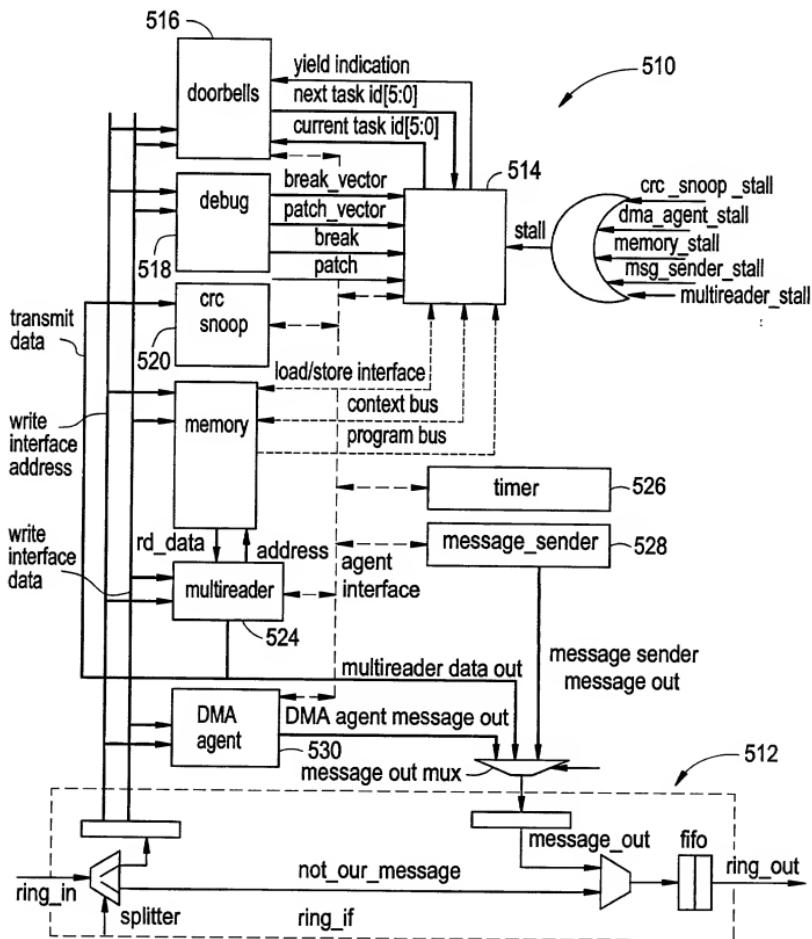


FIG. 47



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FIG. 48

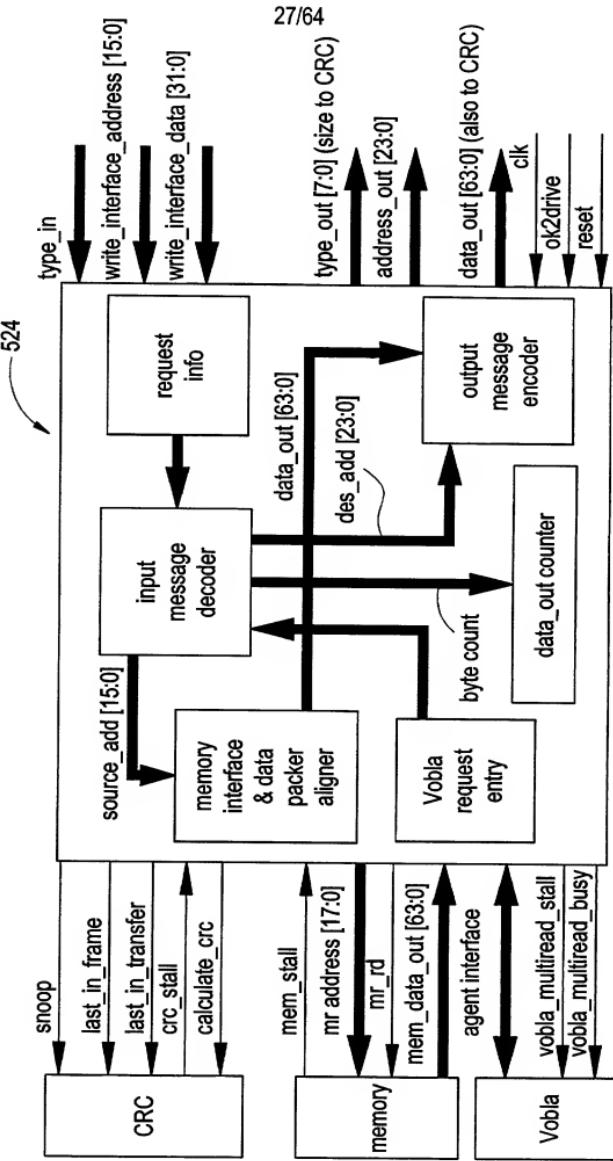


FIG. 49

memory data

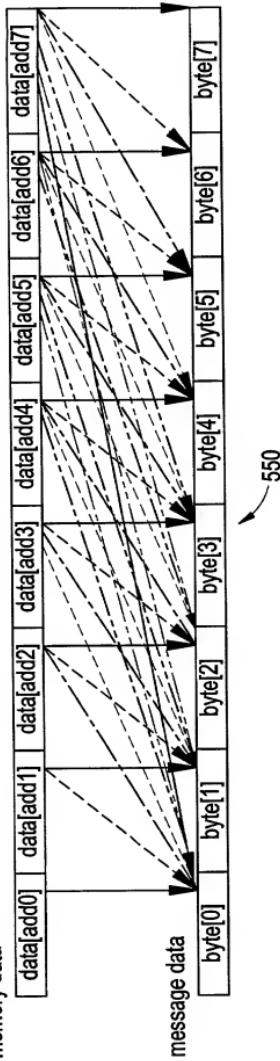


FIG. 50

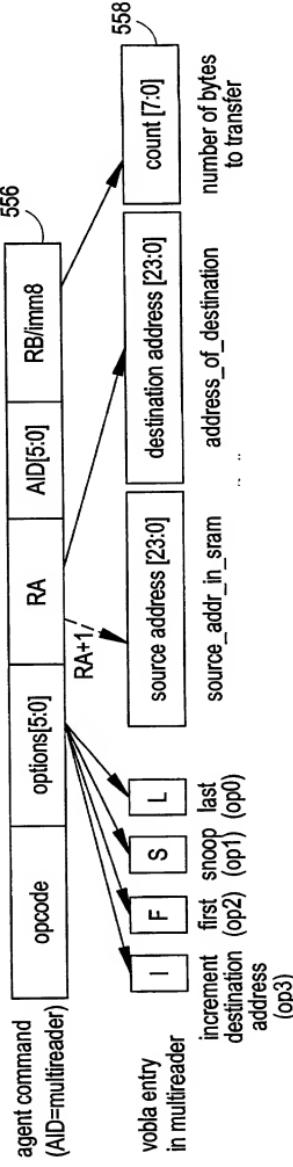
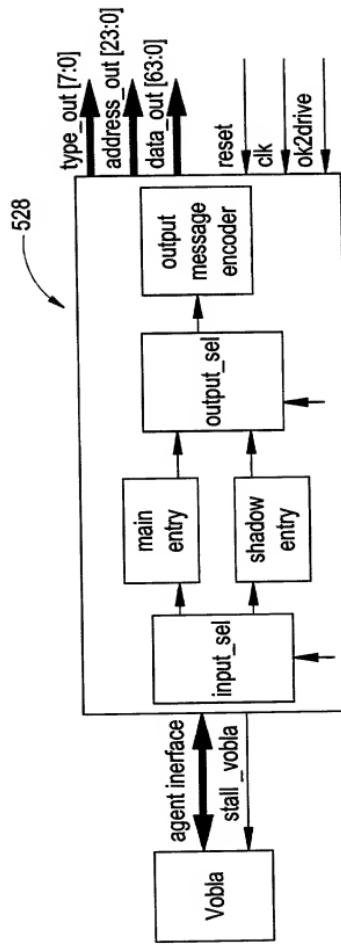




FIG. 51





1000 900 800 700 600

FIG. 52

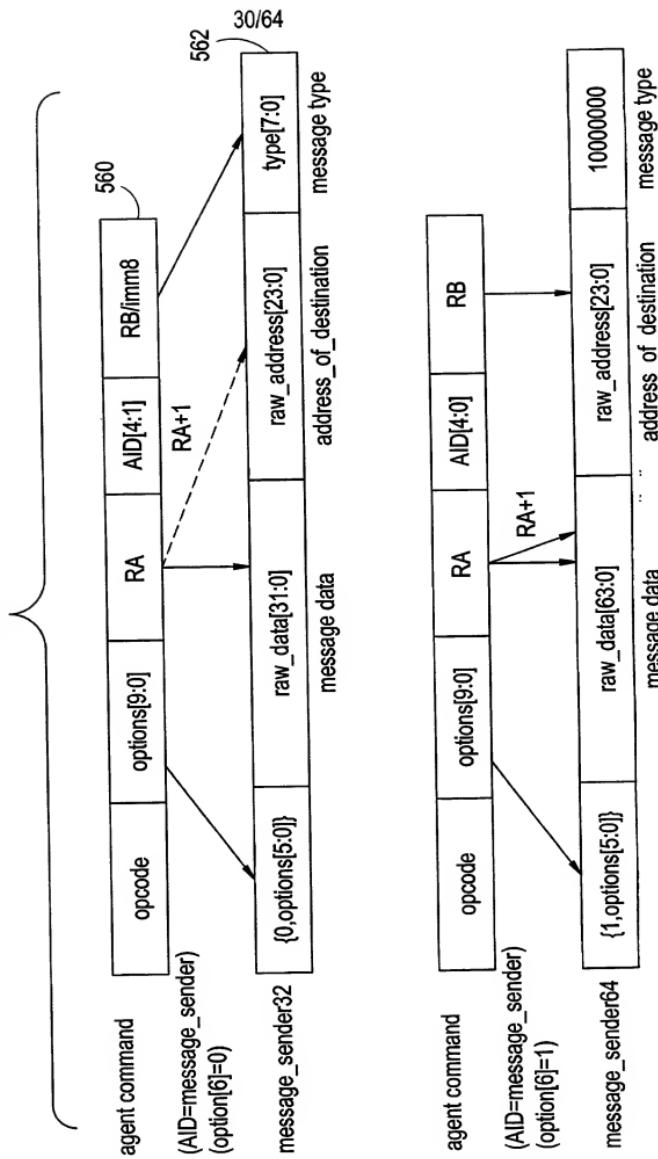


FIG. 53

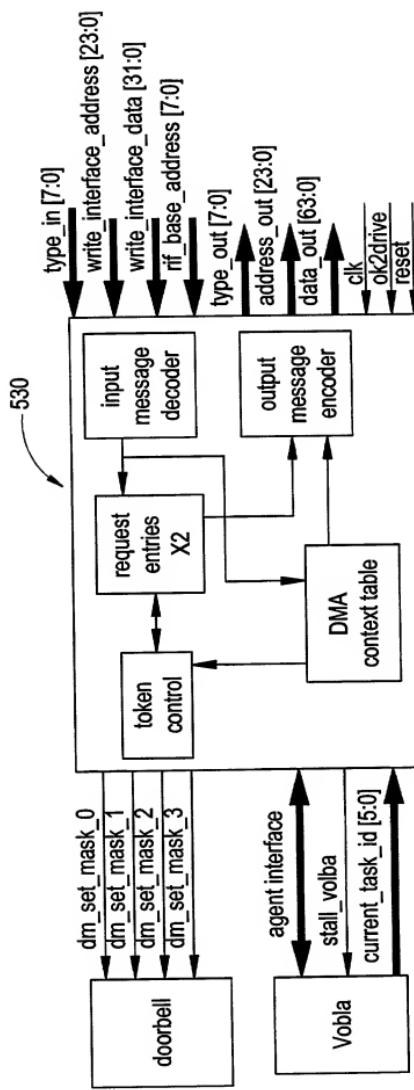




FIG. 54

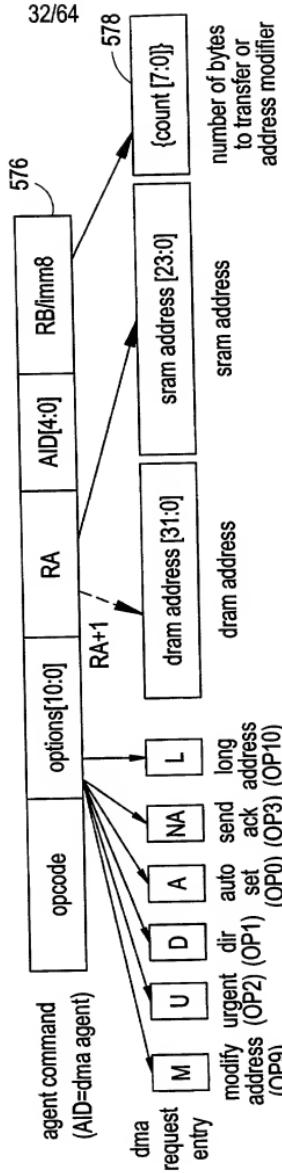


FIG. 55

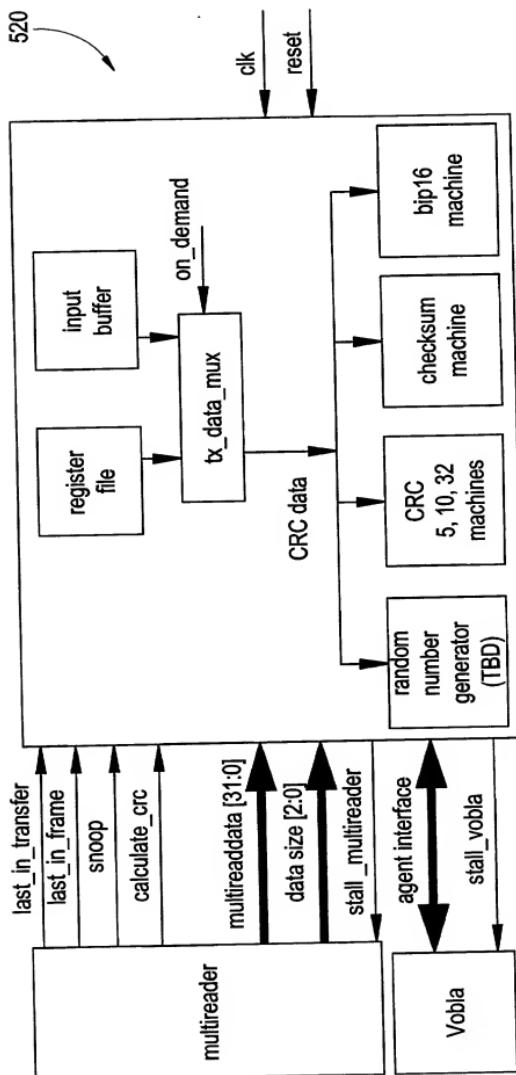


FIG. 56

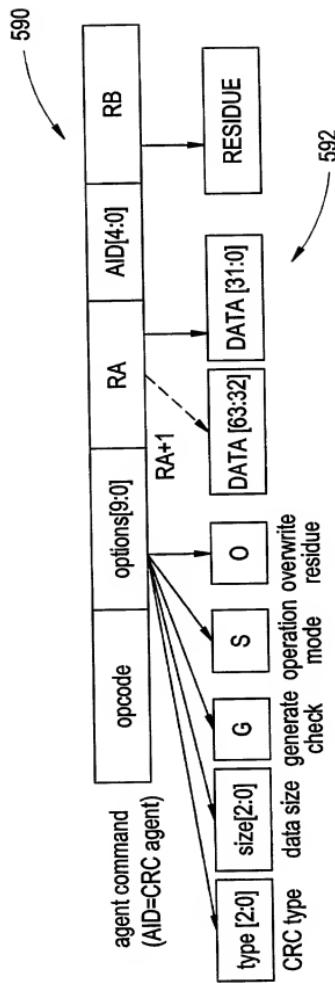


FIG. 57

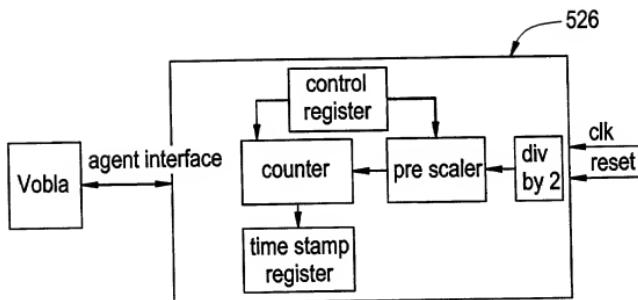


FIG. 58

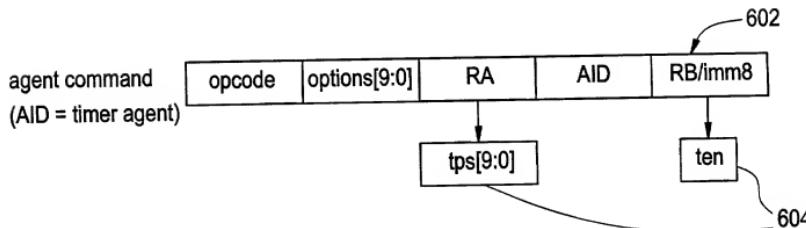




FIG. 59

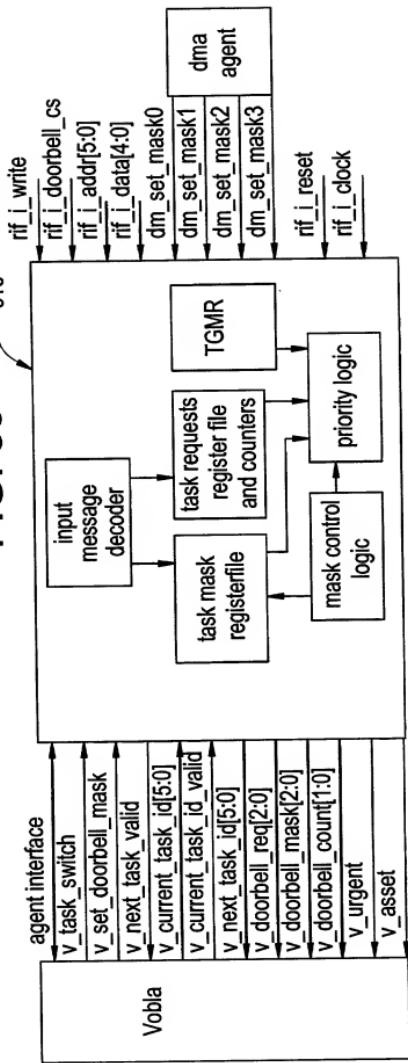


FIG. 60

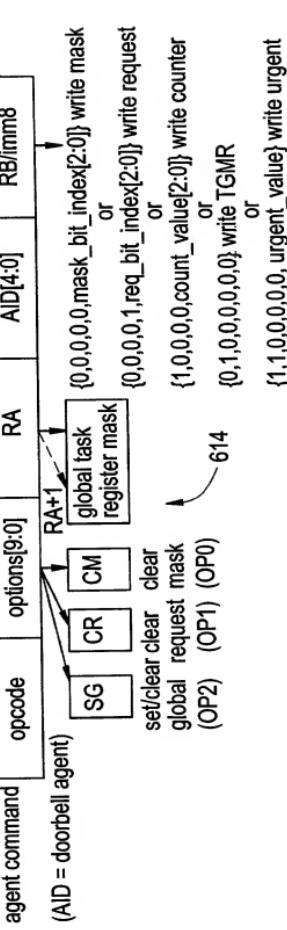
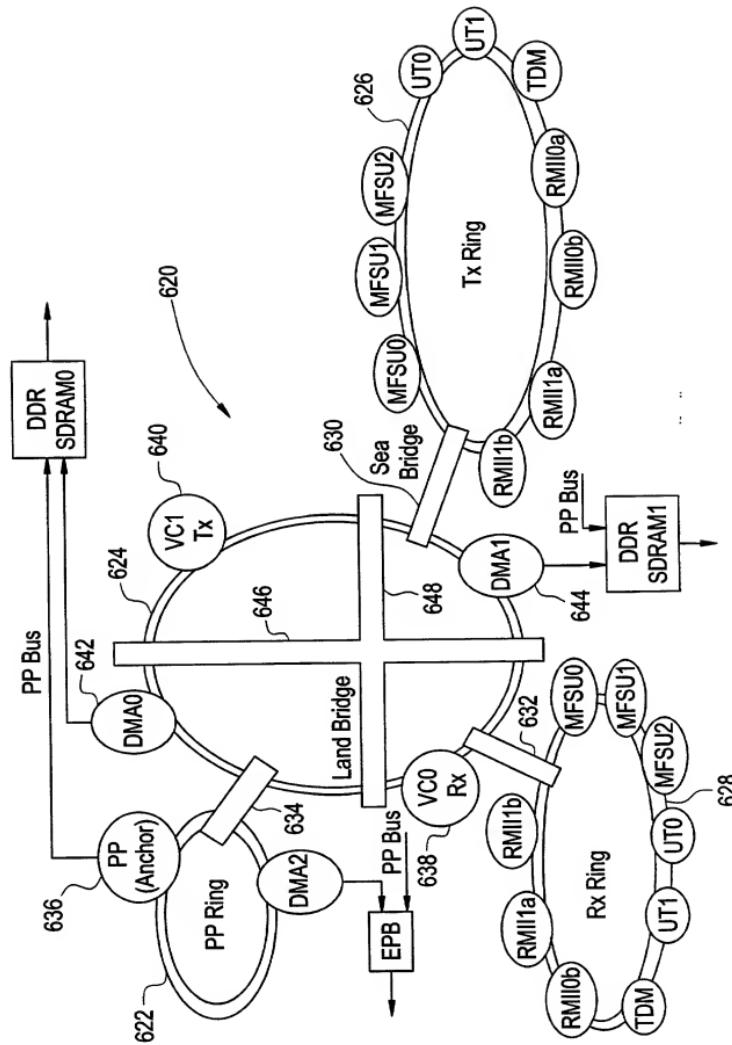




FIG. 61





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FIG. 62

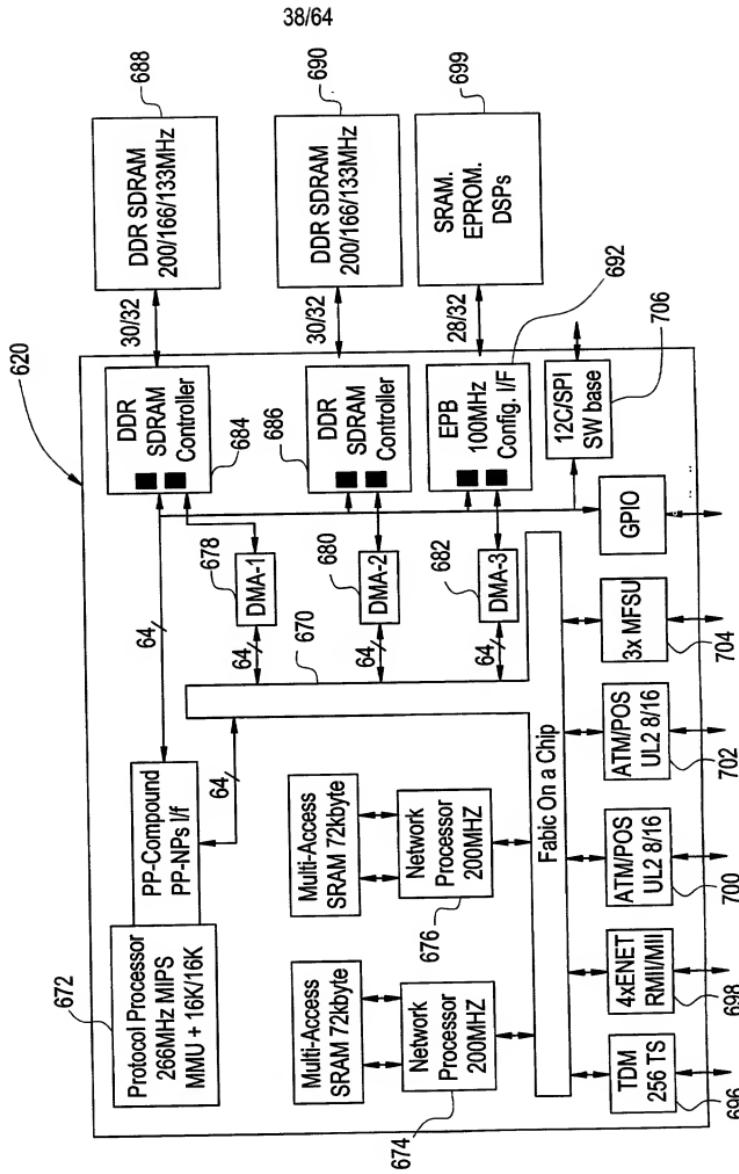
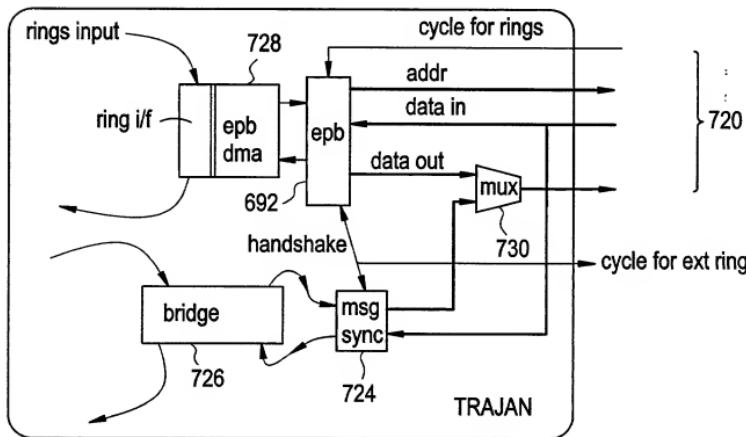
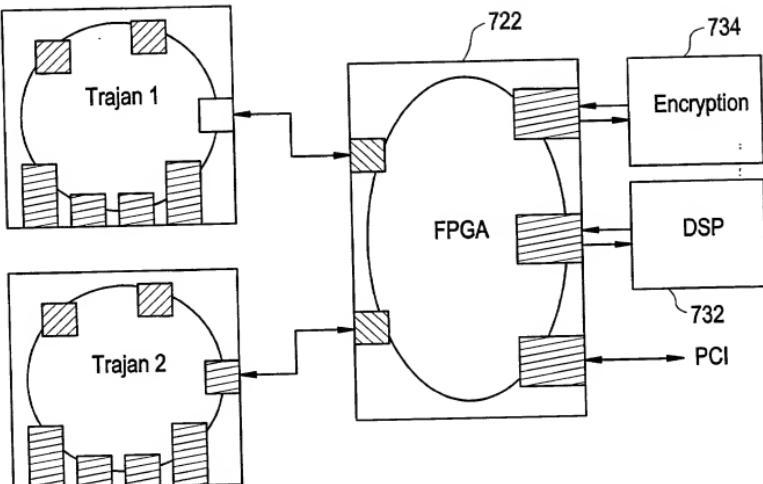


FIG. 63



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FIG. 64



- Processor
- Functional unit
- External ring interface

FIG. 65

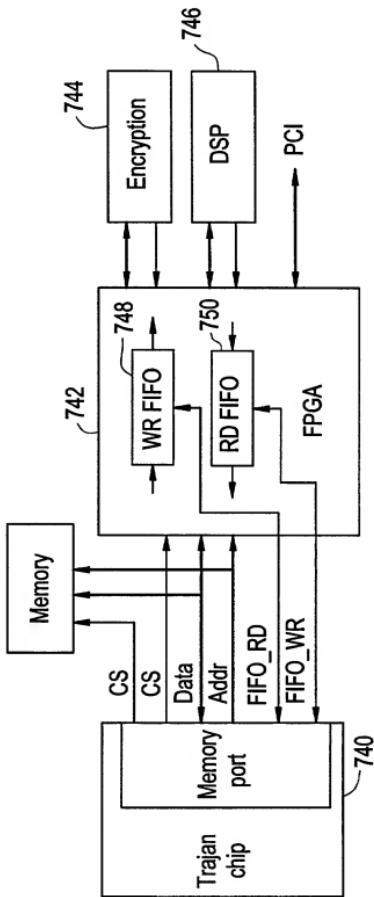


FIG. 66

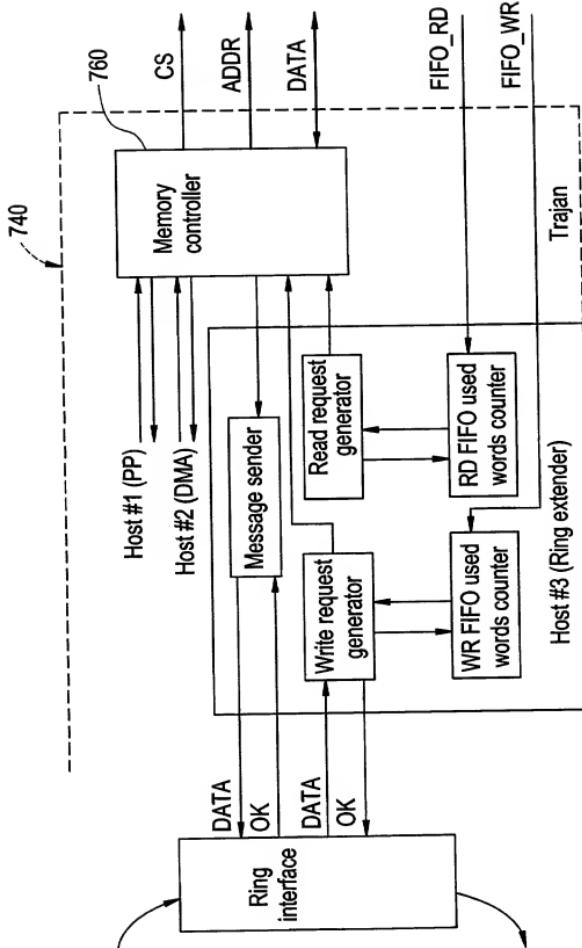
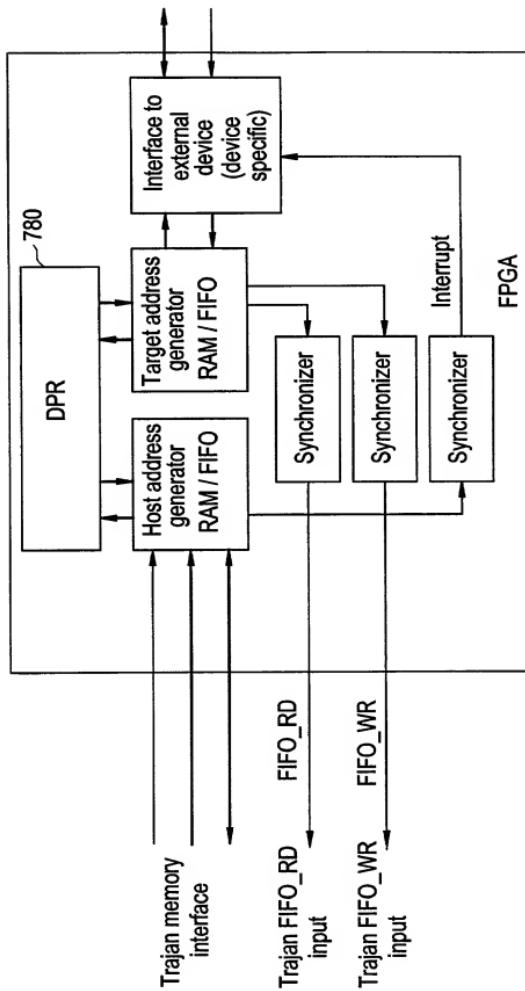


FIG. 67





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FIG. 68

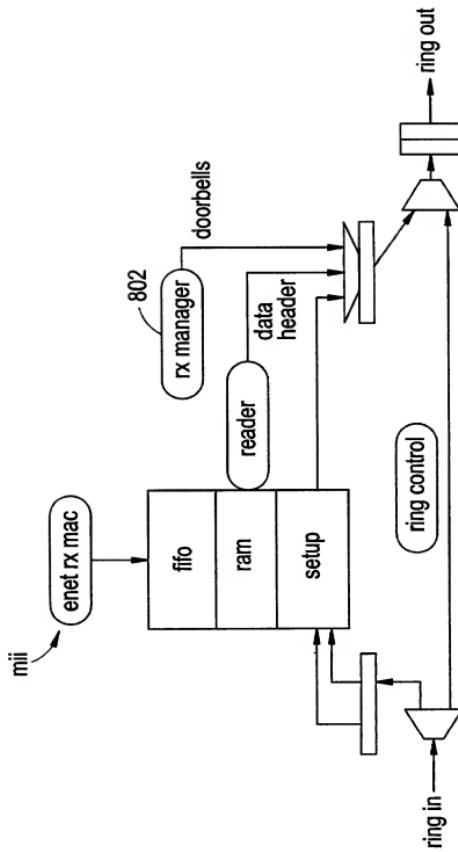


FIG. 69

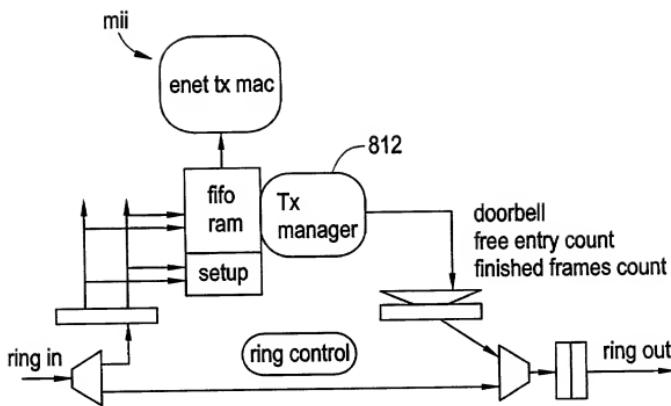


FIG. 70

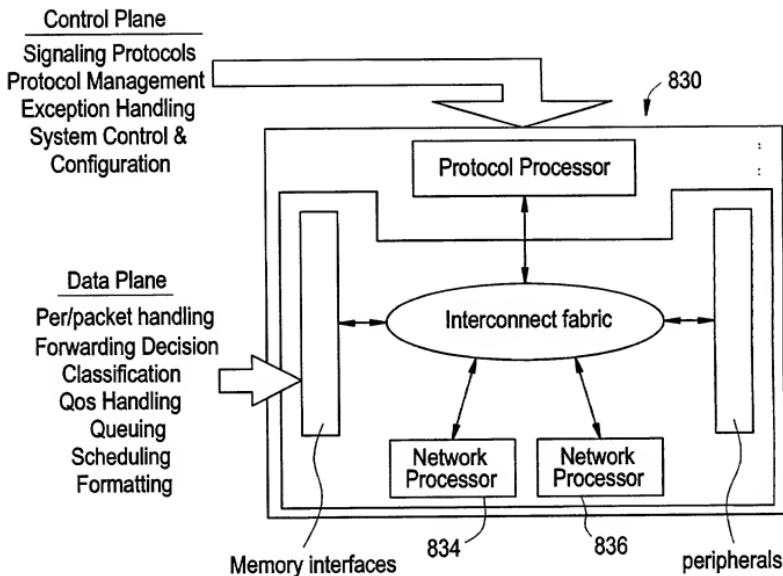


FIG. 71

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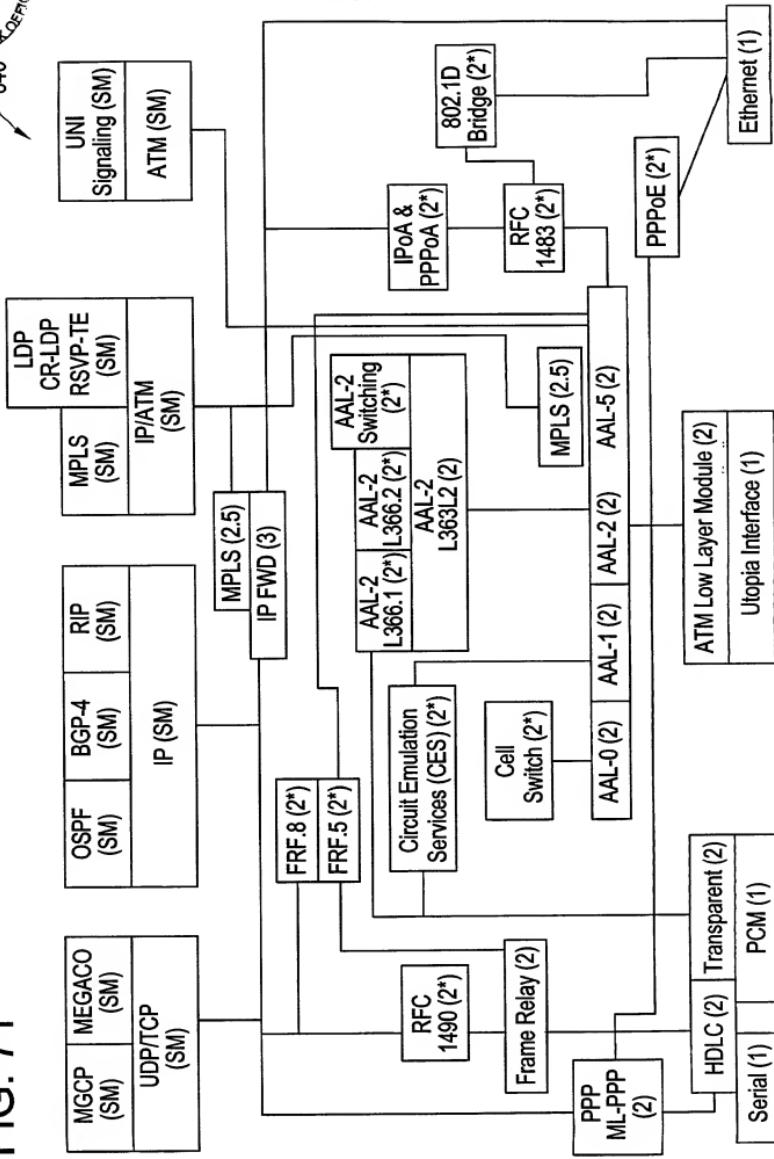


FIG. 72

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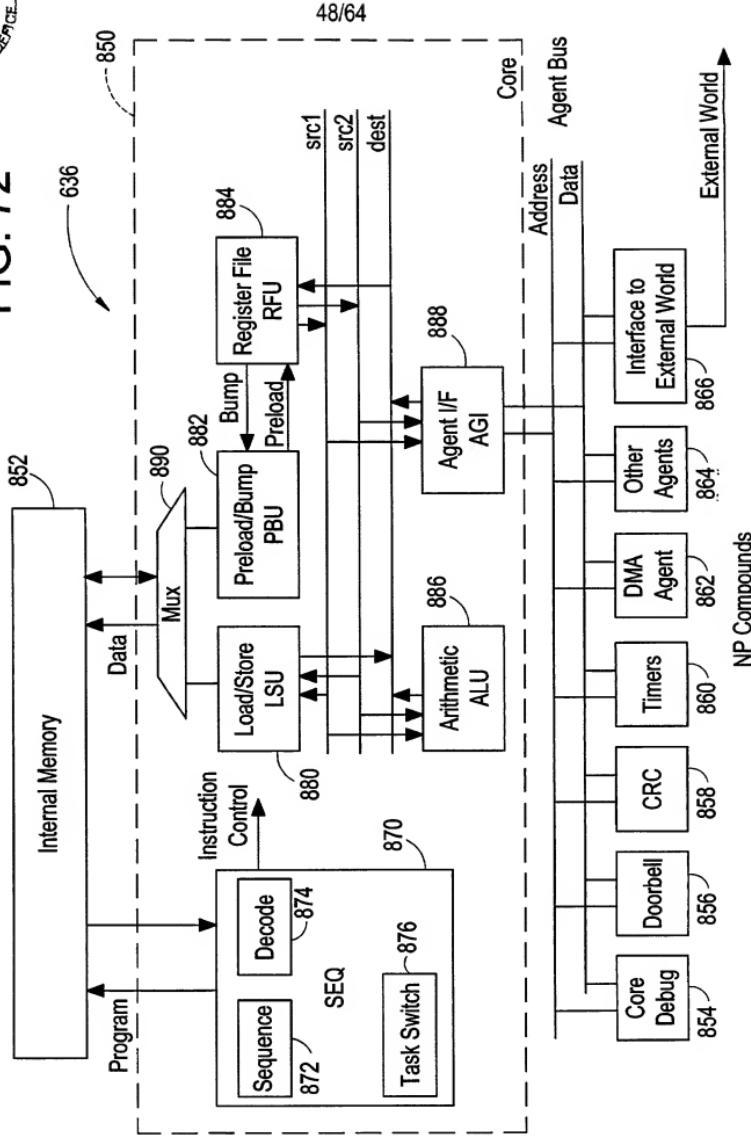


FIG. 73

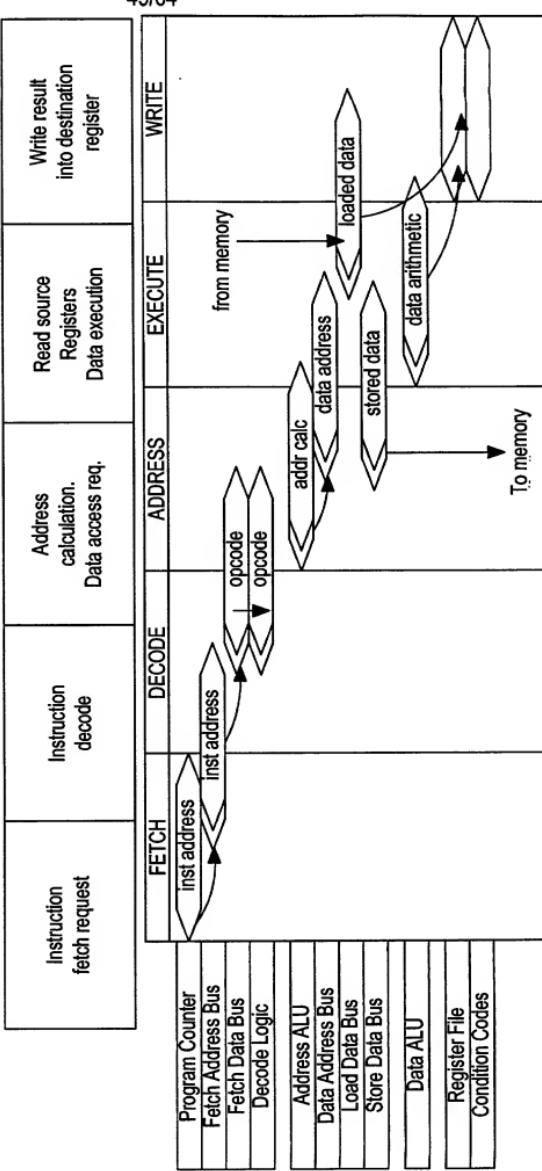
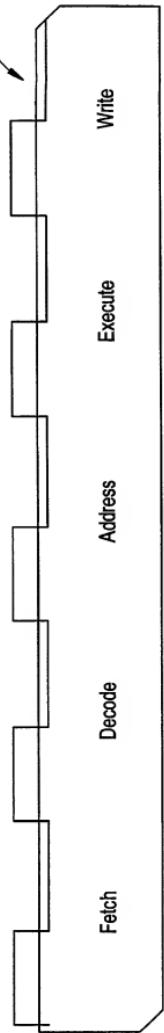


FIG. 74

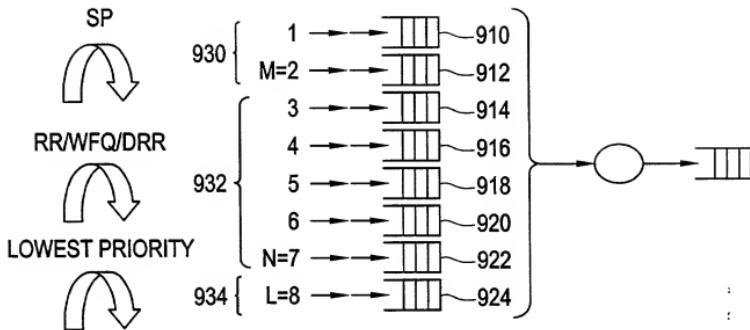


FIG. 75

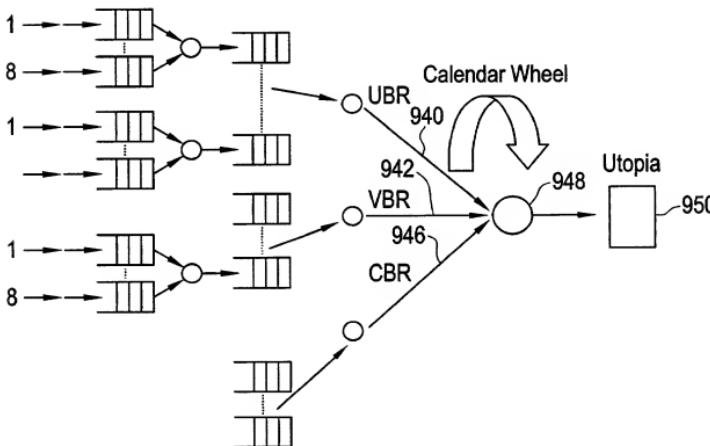


FIG. 76

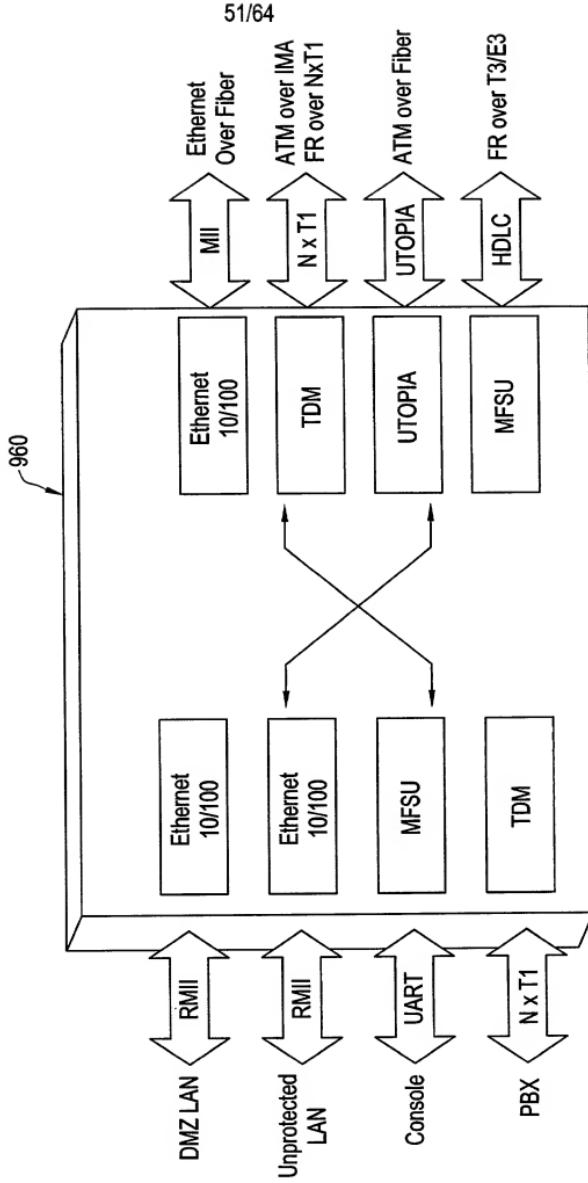




FIG. 77

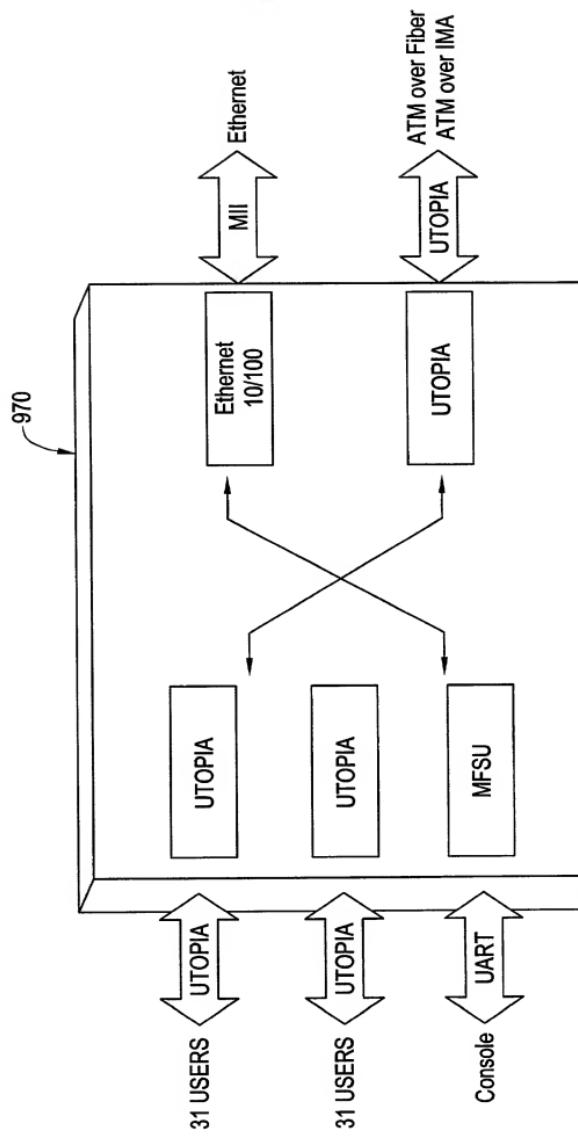
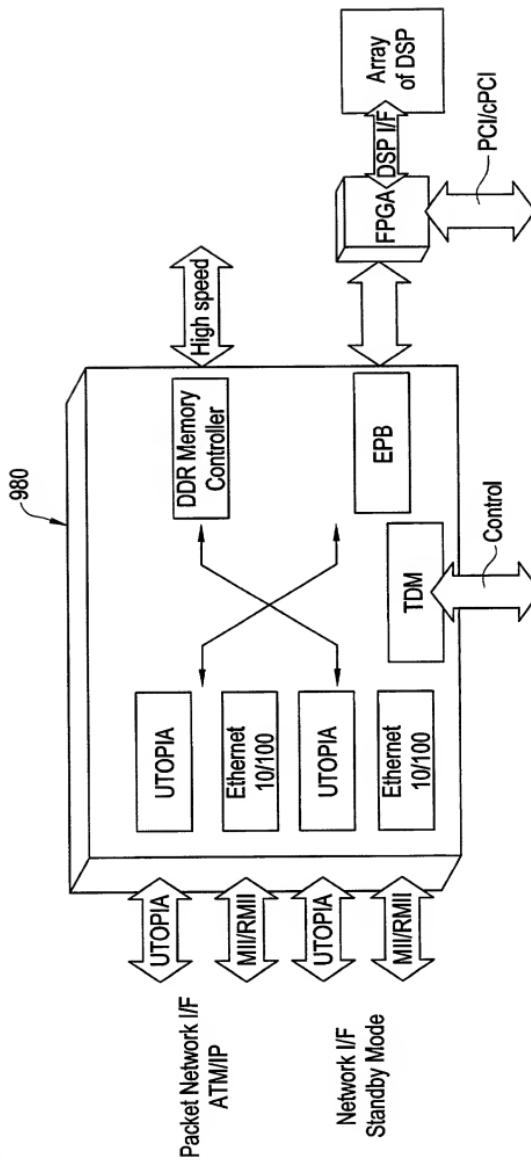


FIG. 78





54/64

FIG. 79

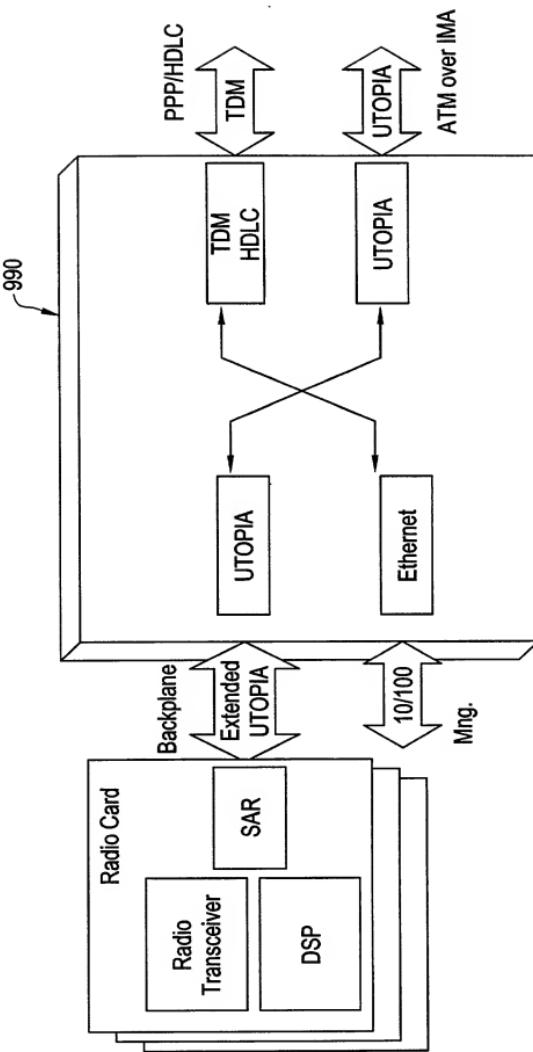




FIG. 80

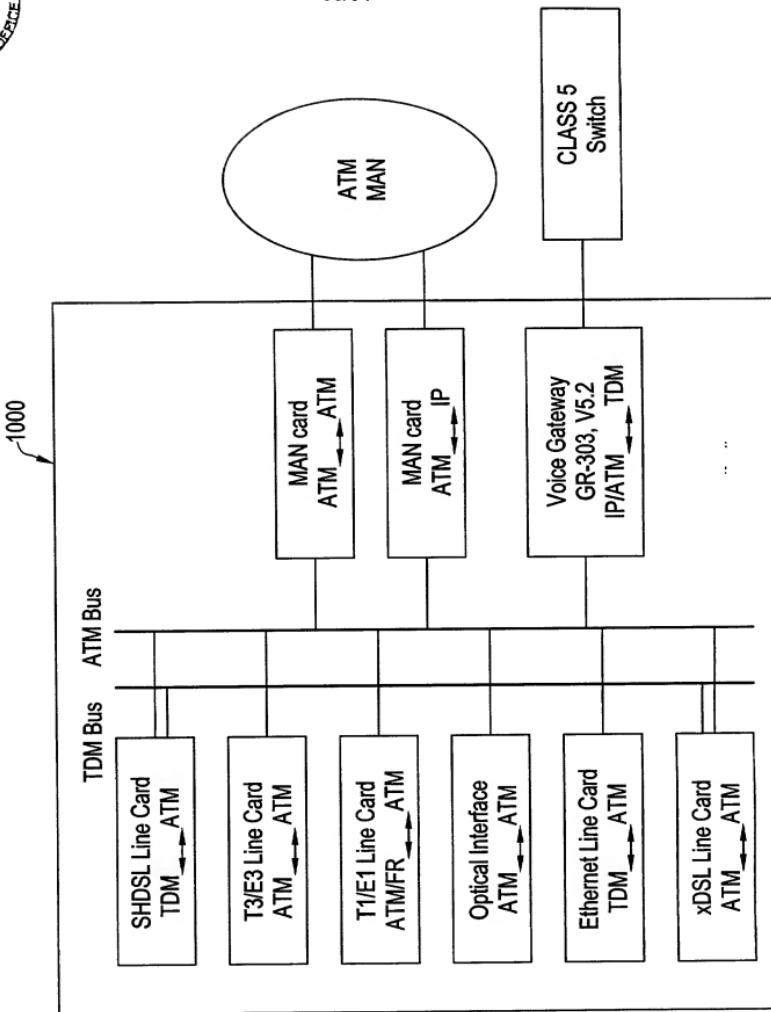




FIG. 81

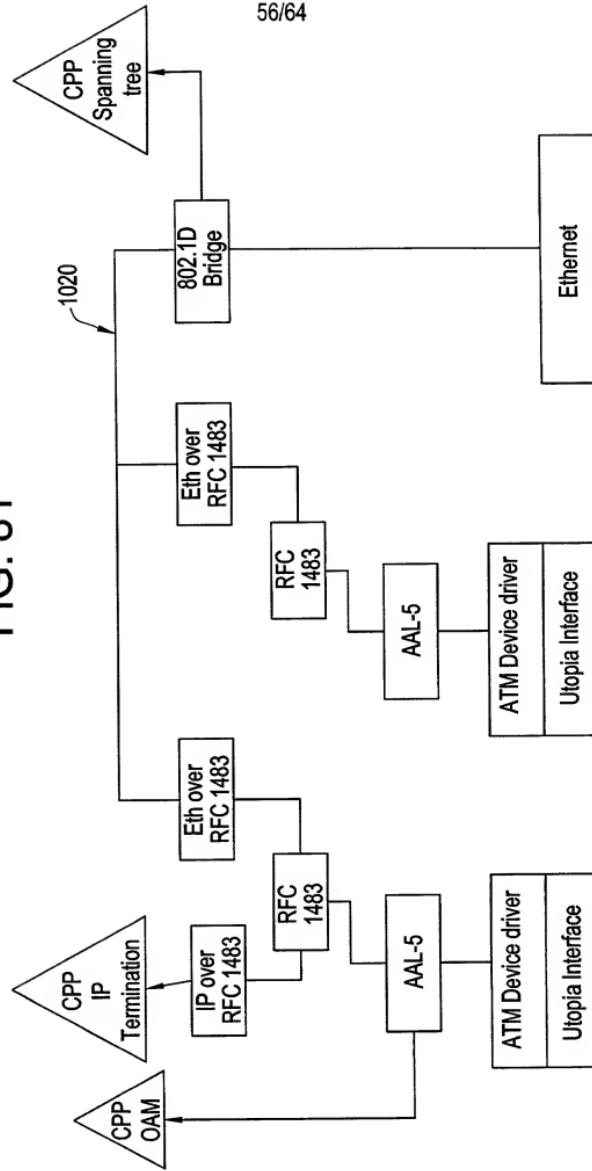


FIG. 82

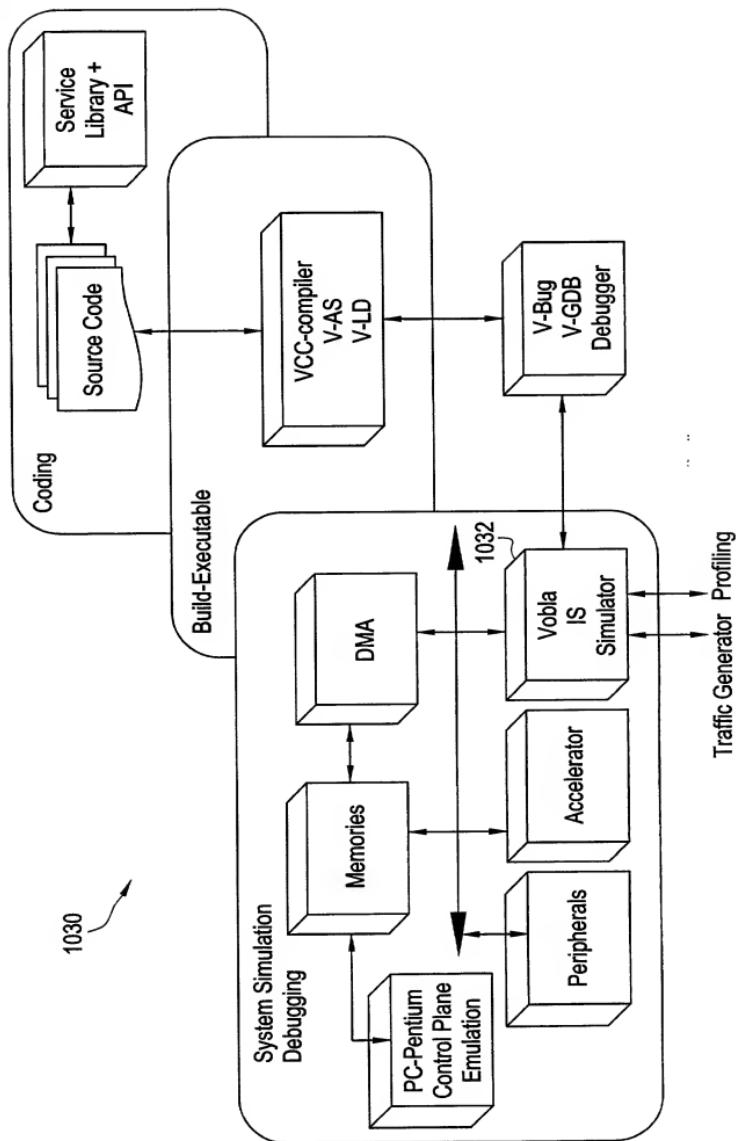


FIG. 83

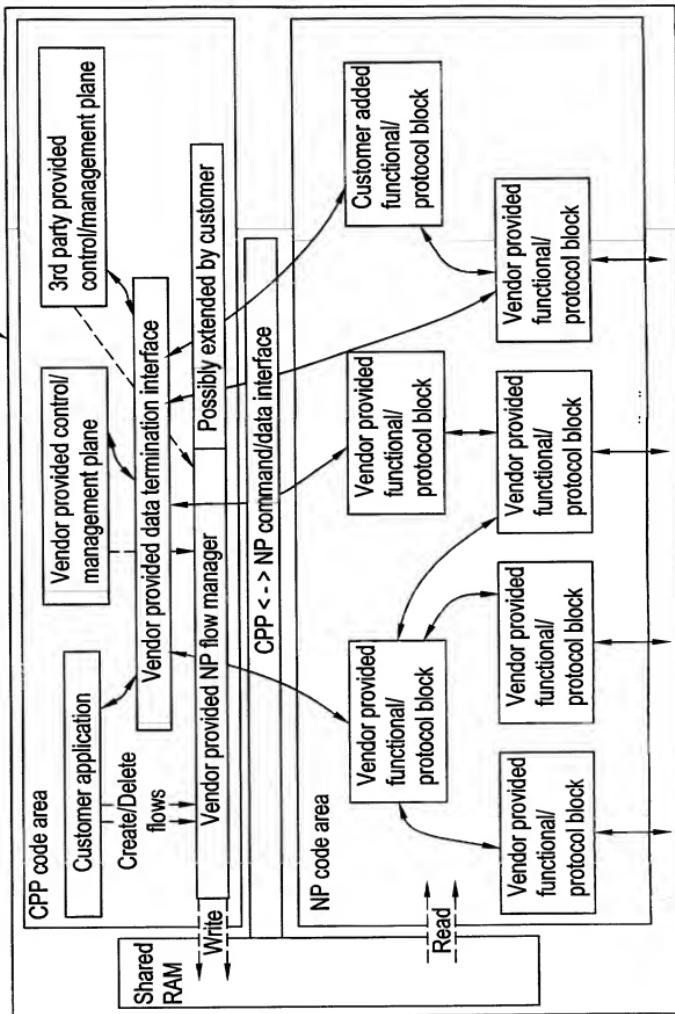


FIG. 84

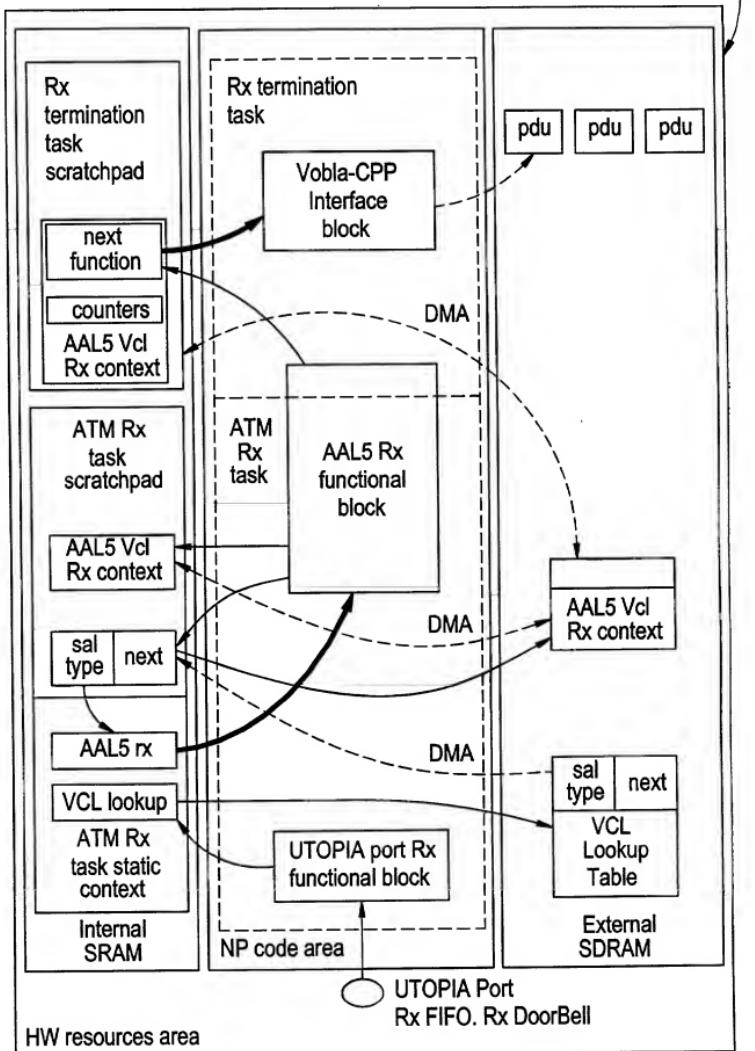


FIG. 85

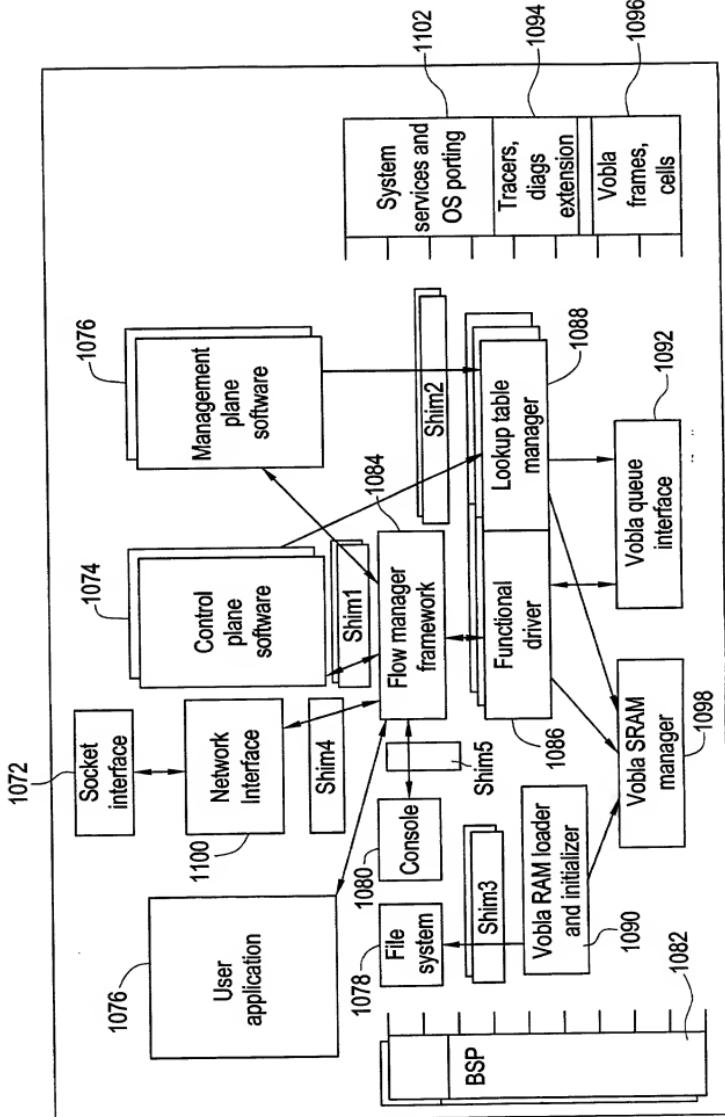
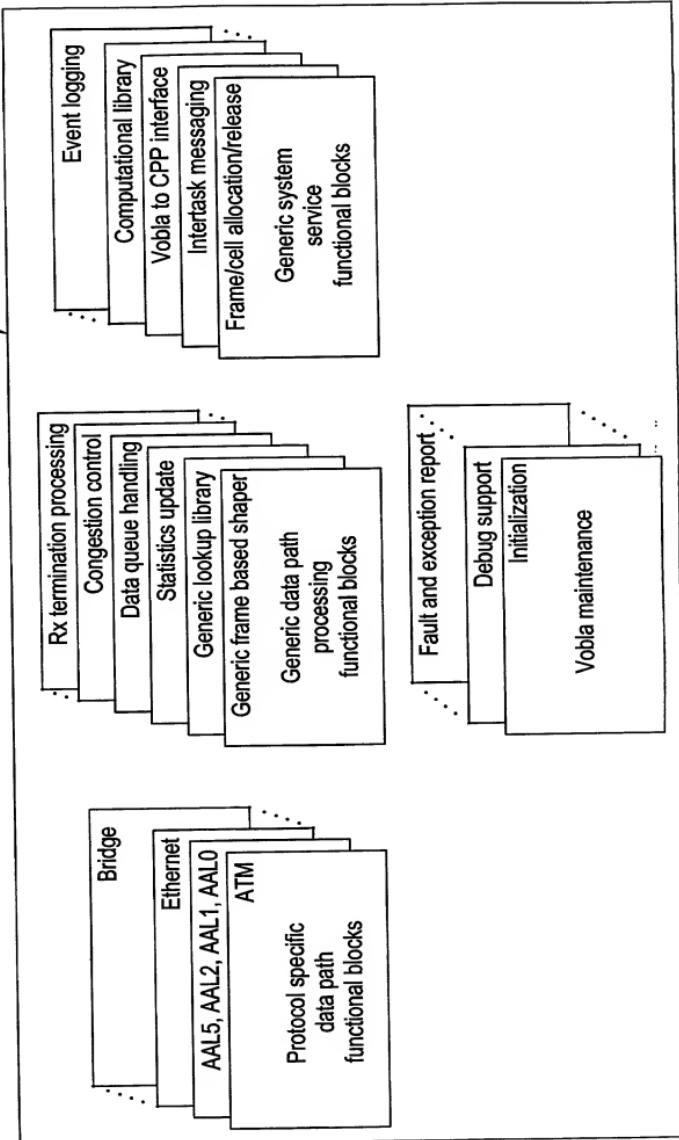


FIG. 86

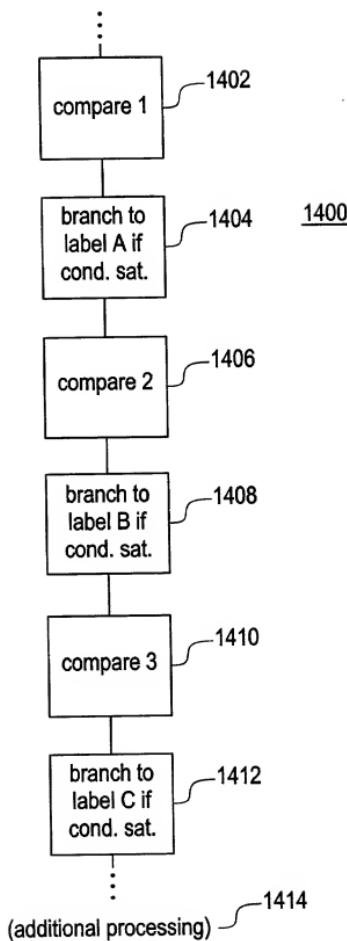
1200





## FIG. 87

## PRIOR ART



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FIG. 88

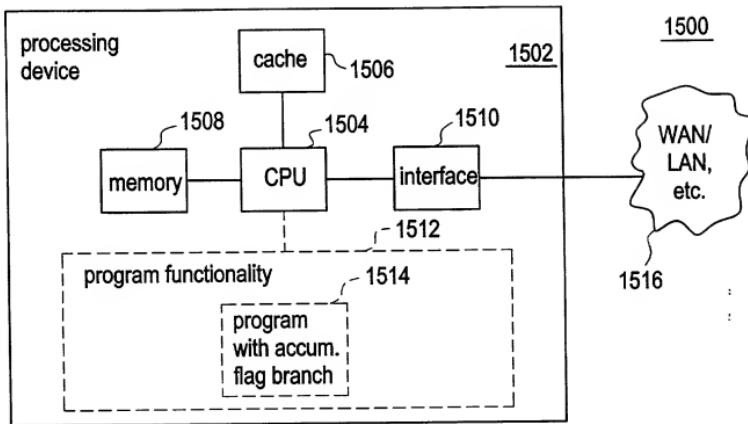


FIG. 89

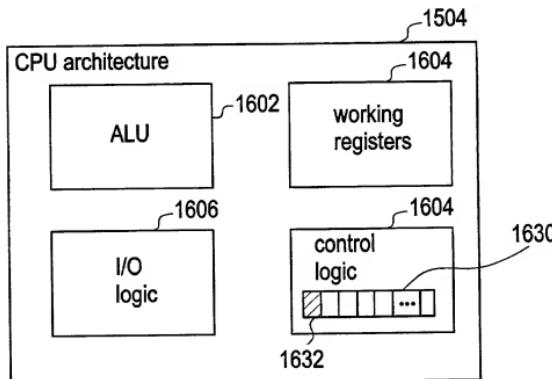


FIG. 90

